



CD6041 CD6042 CD6044

600 nA, Rail-to-Rail Input/Output Op Amps

Version: Rev 1.0.0 Date: 2025-5-26

Features ■■

- Single-Supply Operation from +1.4V ~ +5.5V
- Rail-to-Rail Input / Output
- Gain-Bandwidth Product: 14.5KHz (Typ.)
- Low Input Bias Current: 1pA (Typ.)
- Low Offset Voltage: 3mV (Max.)
- Quiescent Current: 600nA per Amplifier (Typ.)
- Operating Temperature: -40°C ~ +125°C
- Embedded RF Anti-EMI Filter
- Small Package:

CD6041 Available in SOT23-5 and SC70-5

CD6042 Available in SOP-8, MSOP-8 and

CD6044 Available in SOP-14 and TSSOP-14

Application ■■

- ASIC Input or Output Amplifier
- Sensor Interface
- Medical Communication
- Smoke Detectors
- Audio Output
- Piezoelectric Transducer Amplifier
- Medical Instrumentation
- Portable Systems

Description ■■

The CD604X family has a high gain-bandwidth product of 14.5KHz, a slew rate of 6V/ms, and a quiescent current of 600nA/amplifier at 5V. The CD604X family is designed to provide optimal performance in low voltage and low noise systems. They provide rail-to-rail output swing into heavy loads. The input common mode voltage range includes ground, and the maximum input offset voltage is 3mV for CD604X family. They are specified over the extended industrial temperature range (-40°C to +125°C). The operating range is from 1.4V to 5.5V. The CD6041 single is available in Green SC70-5 and SOT23-5 packages. The CD6042 Dual is available in Green SOP-8, MSOP-8 and DFN-8 packages. The CD6044 Quad is available in Green SOP-14 and TSSOP-14 packages.

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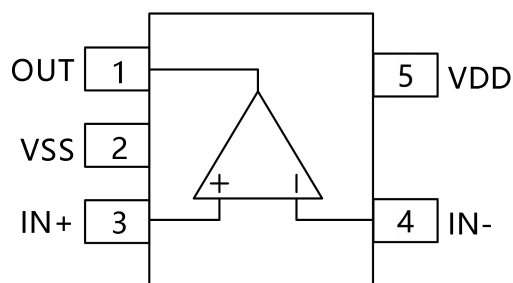
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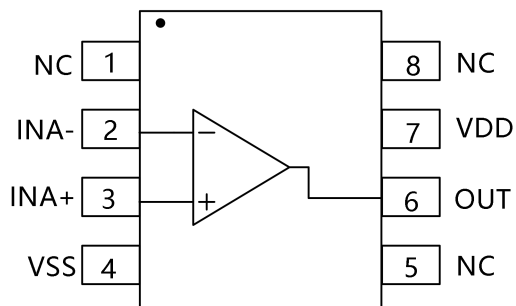
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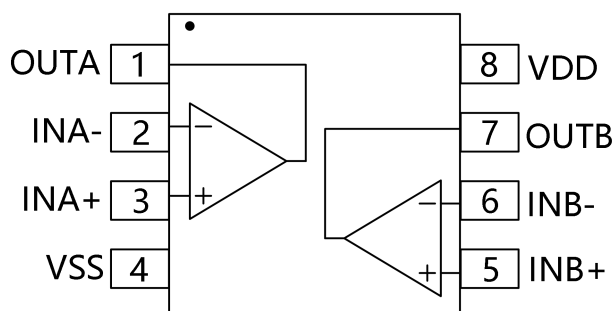
Pin Configurations



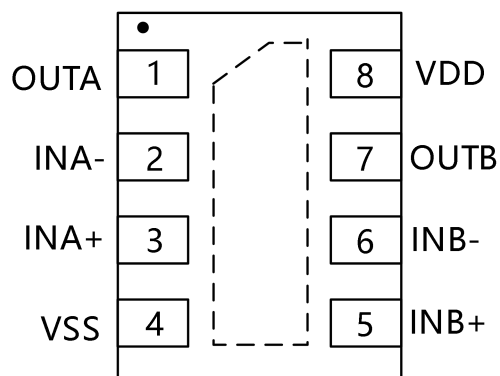
SOT23-5/SC70-5



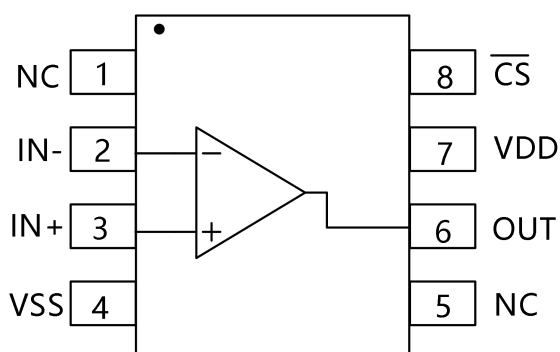
SOT-8/MSOP-8



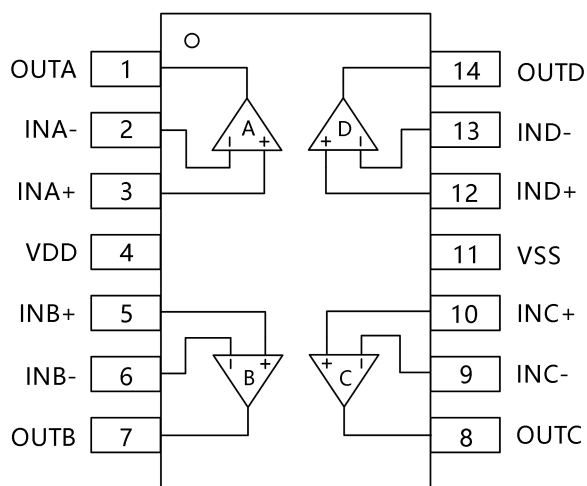
SOP-8/MSOP-8



DNF-8



SOP-8/MSOP-8



SOP-14/TSSOP-14

Absolute Maximum Ratings

Condition	Min	Max
Power Supply Voltage (VDD to VSS)	-0.5V	+7.5V
Analog Input Voltage (IN+ or IN-)	VSS-0.5V	VDD+0.5V
PDB Input Voltage	VSS-0.5V	+7V
Operating Temperature Range	-40°C	+125°C
Junction Temperature	+160°C	
Storage Temperature Range	-55°C	+150°C
Lead Temperature (soldering, 10sec)	+260°C	
Package Thermal Resistance (TA= +25°C)		
SOP-8, θ_{JA}	125°C/W	
MSOP-8, θ_{JA}	216°C/W	
SOT23-5, θ_{JA}	190°C/W	
SC70-5, θ_{JA}	333°C/W	
ESD Susceptibility		
HBM	6KV	
MM	300V	

Electrical characteristics

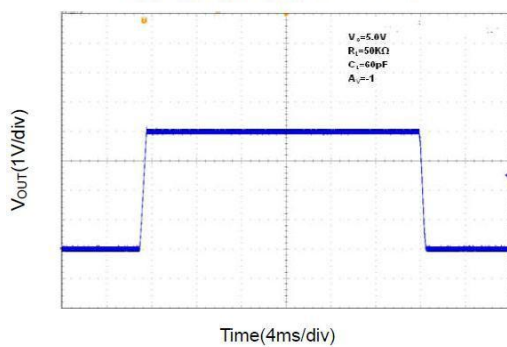
At $V_S = +5V$, $R_L = 1M\Omega$ connected to $V_S/2$, and $V_{OUT} = V_S/2$, unless otherwise noted.

Parameter	Symbol	Conditions	Typ	Min	Max	Unit
INPUT CHARACTERISTICS						
Input Offset Voltage	V_{OS}	$V_{CM} = V_S/2$	0.4	-3	3	mV
Input Bias Current	I_B		1	--	--	pA
Input Offset Current	I_{OS}		1	--	--	pA
Common-Mode Voltage Range	V_{CM}	$V_S = 5.5V$	-0.1 to +5.6	--	--	V
Common-Mode Rejection Ratio	C_{MRR}	$V_S = 5V, V_{CM} = -0.1V$ to 2.5V	78	66	--	dB
		$V_S = 5V, V_{CM} = -0.1V$ to 5.1V	84	67	--	
Open-Loop Voltage Gain	A_{OL}	$V_S=1.4V, R_L = 50k\Omega, V_O = V_S-0.1V$	86	75	--	dB

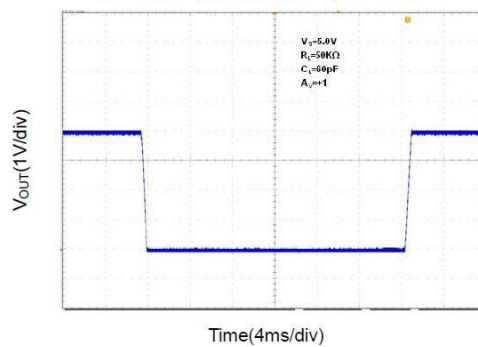
		$V_S=5V, R_L = 50k\Omega, V_O = V_S-0.1V$	93	84	--	
Input Offset Voltage Drift	$\Delta V_{OS}/\Delta T$		2.5	--	--	$\mu V/^\circ C$
OUTPUT CHARACTERISTICS						
Output Voltage Swing from Rail	V_{OH}	$V_S=1.4V, R_L = 50k\Omega$	1.395	1.390	--	V
	V_{OL}		4.5	--	10	mV
	V_{OH}	$V_S=5V, R_L = 50k\Omega$	4.997	4.990	--	V
	V_{OL}		3.5	--	10	mV
Output Current	I_{SOURCE}	$R_L = 10\Omega \text{ to } V_S/2$	20	--	--	mA
	I_{SINK}		20	--	--	
POWER SUPPLY						
Operating Voltage Range			1.4	--	--	V
			5.5	--	--	V
Power Supply Rejection Ratio	P_{SRR}	$V_S=+1.4V \text{ to } +5.5V, V_{CM}=+0.5V$	80	77	--	dB
Quiescent Current / Amplifier	I_Q		600	--	--	nA
DYNAMIC PERFORMANCE (CL = 100pF)						
Gain-Bandwidth Product	G_{BP}		14.5	--	--	KHz
Slew Rate	S_R	G = +1, 2V Output Step	6	--	--	V/ms

Typical Characteristics

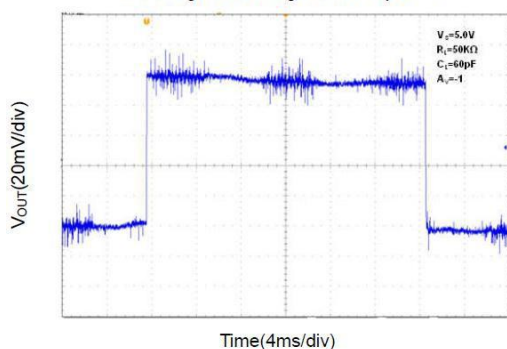
Large Signal Inverting Pulse Response



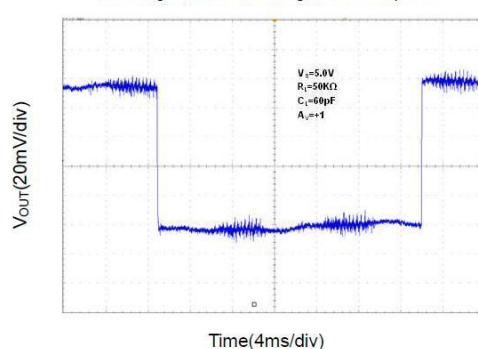
Large Signal Non-Inverting Pulse Response



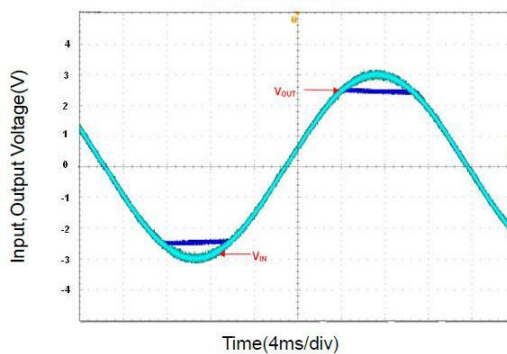
Small Signal Inverting Pulse Response



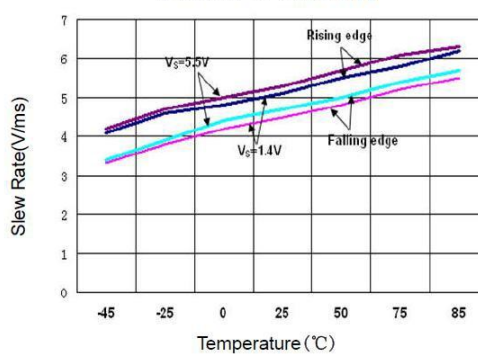
Small Signal Non-Inverting Pulse Response



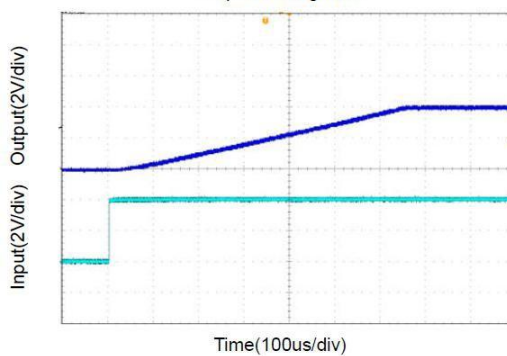
No Phase Reversal



Slew Rate vs. Temperature



Output Settling Time



Applications Information ■

Size

CD6041 family series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. The small footprints of the CD6041 family packages save space on printed circuit boards and enable the design of smaller electronic products.

Power Supply Bypassing and Board Layout

CD6041 family series operates from a single 1.4V to 5.5V supply or dual $\pm 0.7V$ to $\pm 2.75V$ supplies. For best performance, a 0.1 μF ceramic capacitor should be placed close to the VDD pin in single supply operation. For dual supply operation, both VDD and VSS supplies should be bypassed to ground with separate 0.1 μF ceramic capacitors.

Low Supply Current

The low supply current (typical 500nA per channel) of CD6041 family will help to maximize battery life. They are ideal for battery powered systems

Operating Voltage

CD6041 family operates under wide input supply voltage (1.4V to 5.5V). In addition, all temperature specifications apply from $-40^{\circ}C$ to $+125^{\circ}C$. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime

Rail-to-Rail Input

CD6041 family operates under wide input supply voltage (1.4V to 5.5V). In addition, all temperature specifications apply from $-40^{\circ}C$ to $+125^{\circ}C$. Most behavior remains unchanged throughout the full operating voltage range. These guarantees ensure operation throughout the single Li-Ion battery lifetime

Rail-to-Rail Input

The input common-mode range of CD6041 family extends 100mV beyond the supply rails ($VSS-0.1V$ to $VDD+0.1V$). This is achieved by using complementary input stage. For normal operation, inputs should be limited to this range.

Rail-to-Rail Output

Rail-to-Rail output swing provides maximum possible dynamic range at the output. This is particularly important when operating in low supply voltages. The output voltage of CD6041 family can typically swing to less than 10mV from supply rail in light resistive loads ($>50k\Omega$).

Capacitive Load Tolerance

The CD6041 family is optimized for bandwidth and speed, not for driving capacitive loads. Output capacitance will create a pole in the amplifier's feedback path, leading to excessive peaking and potential oscillation. If dealing with load capacitance is a requirement of the application, the two strategies to consider are (1) using a small resistor in series with the amplifier's output and the load capacitance and (2) reducing the bandwidth of the amplifier's

feedback loop by increasing the overall noise gain. Figure 2. shows a unity gain follower using the series resistor strategy. The resistor isolates the output from the capacitance and, more importantly, creates a zero in the feedback path that compensates for the pole created by the output capacitance.

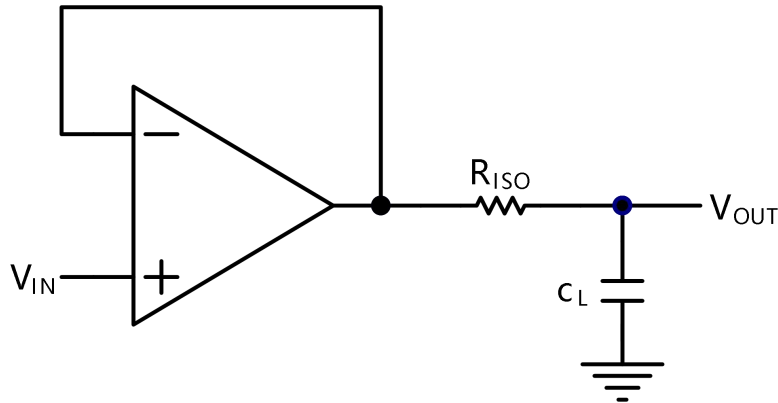


Figure 2. Indirectly Driving a Capacitive Load Using Isolation Resistor

The bigger the R_{ISO} resistor value, the more stable V_{OUT} will be. However, if there is a resistive load R_L in parallel with the capacitive load, a voltage divider (proportional to R_{ISO}/R_L) is formed, this will result in a gain error.

The circuit in Figure 3 is an improvement to the one in Figure 2. R_F provides the DC accuracy by feed-forward the V_{IN} to R_L . C_F and R_{ISO} serve to counteract the loss of phase margin by feeding the high frequency component of the output signal back to the amplifier's inverting input, thereby preserving the phase margin in the overall feedback loop. Capacitive drive can be increased by increasing the value of C_F . This in turn will slow down the pulse response.

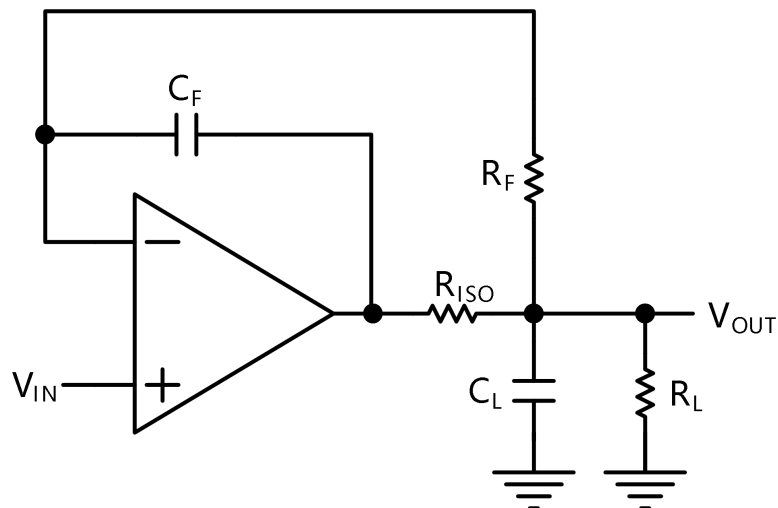


Figure 3. Indirectly Driving a Capacitive Load with DC Accuracy

Typical Application Circuits

Differential amplifier

The differential amplifier allows the subtraction of two input voltages or cancellation of a signal common to the two inputs. It is useful as a computational amplifier in making a differential to single-end conversion or in rejecting a common mode signal. Figure 4. shown the differential amplifier using CD6041 family.

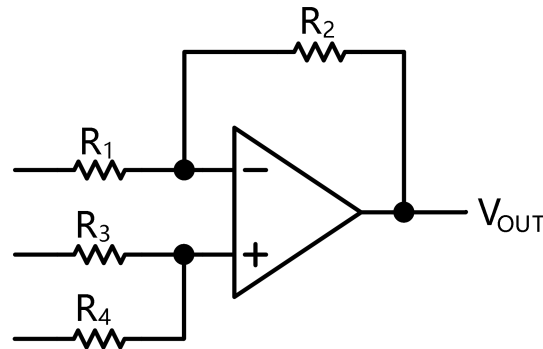


Figure 4. Differential Amplifier

$$V_{OUT} = \left(\frac{R_1 + R_2}{R_3 + R_4} \right) \frac{R_4}{R_1} V_{IN} - \frac{R_2}{R_1} V_{IP} + \left(\frac{R_1 + R_2}{R_3 + R_4} \right) \frac{R_3}{R_1} V_{REF}$$

If the resistor ratios are equal (i.e. $R_1=R_3$ and $R_2=R_4$), then

$$V_{OUT} = \frac{R_2}{R_1} (V_{IP} - V_{IN}) + V_{REF}$$

Low Pass Active Filter

The low pass active filter is shown in Figure 5. The DC gain is defined by $-R_2/R_1$. The filter has a -20dB/decade roll-off after its corner frequency $f_C = 1/(2\pi R_3 C_1)$.

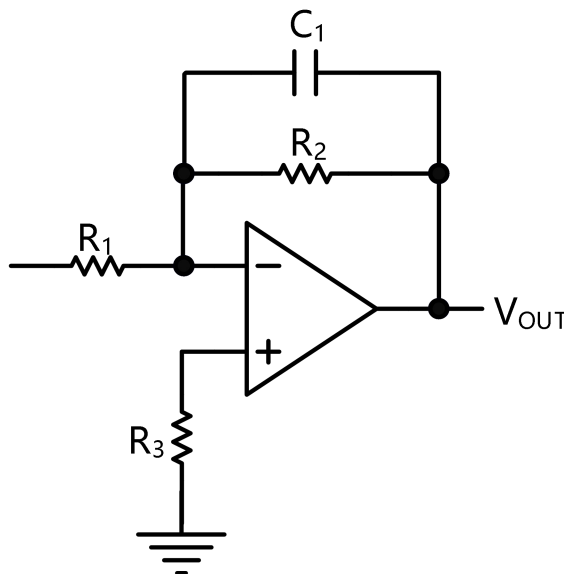


Figure 5. Low Pass Active Filter

Instrumentation Amplifier

The triple CD6041 family can be used to build a three-op-amp instrumentation amplifier as shown in Figure 6. The amplifier in Figure 6 is a high input impedance differential amplifier with gain of R_2/R_1 . The two differential voltage followers assure the high input impedance of the amplifier.

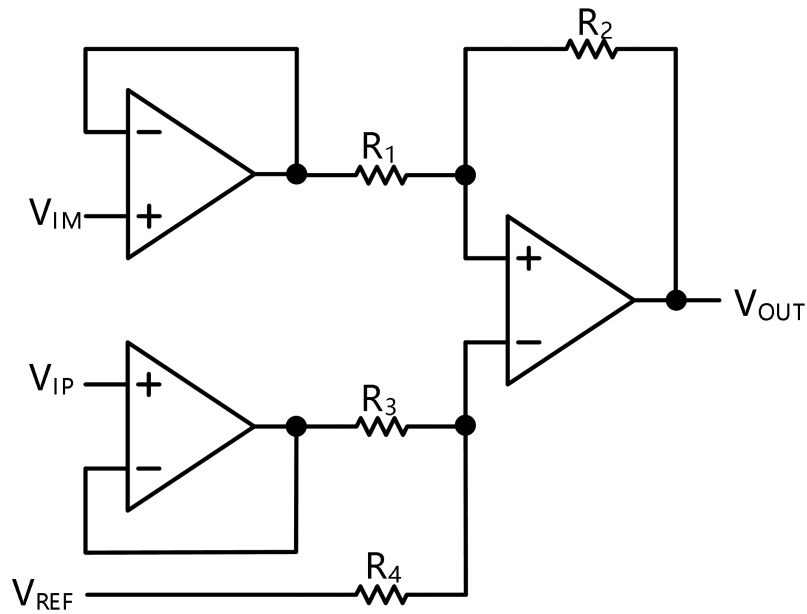
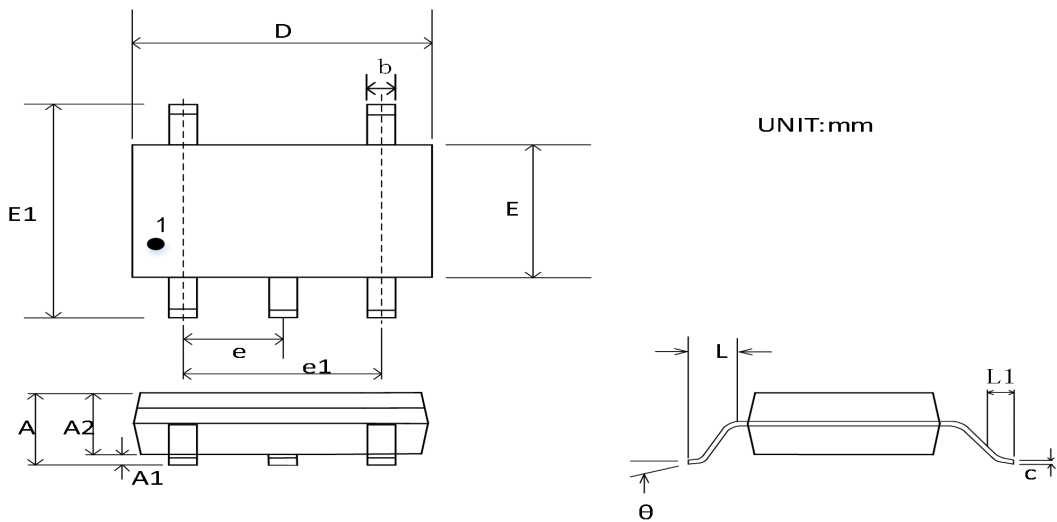


Figure 6. Instrument Amplifier

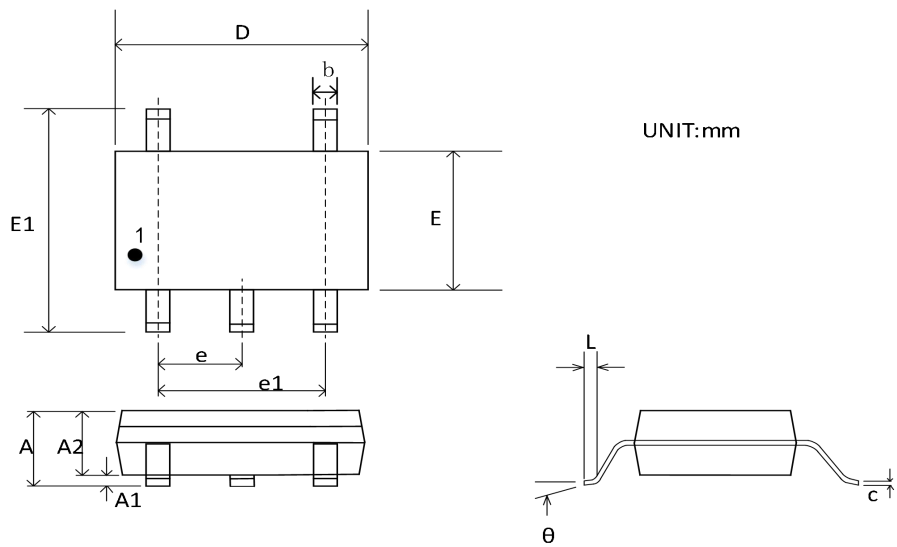
Package Outline Dimensions

SC70-5



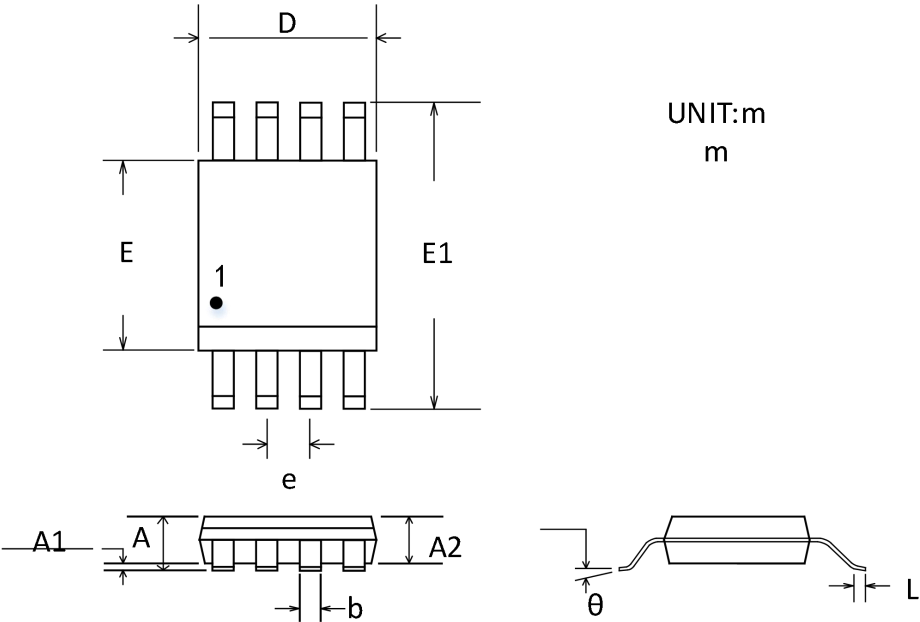
Symbol	Dimensions In Millimeters	
	Min	Max
A	0.800	1.100
A1	0.000	0.100
A2	0.700	1.000
b	0.150	0.300
c	0.080	0.220
D	1.800	2.200
E	1.150	1.350
E1	1.800	2.400
e	0.650 BSC	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

SOT23-5



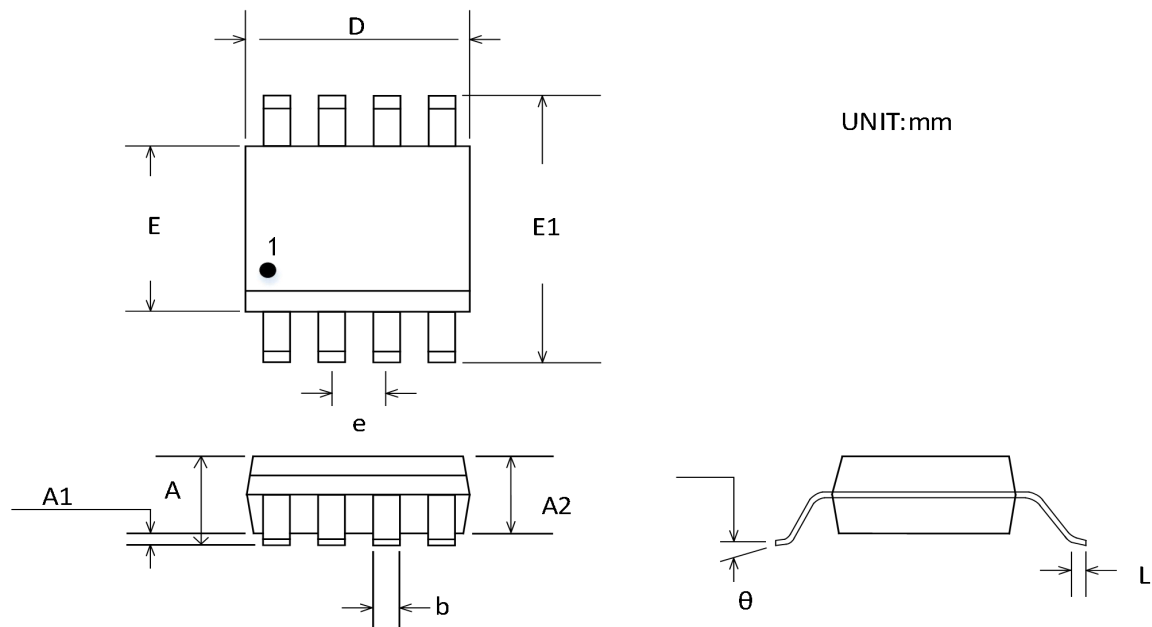
Symbol	Dimensions In Millimeters	
	Min	Max
A	--	1.450
A1	--	0.150
A2	0.900	1.300
b	0.300	0.500
c	0.080	0.220
D	2.900 BSC	
E	1.600 BSC	
E1	2.80 BSC	
e	0.950 BSC	
e1	1.900 BSC	
L	0.300	0.600
θ	0°	8°

MSOP-8



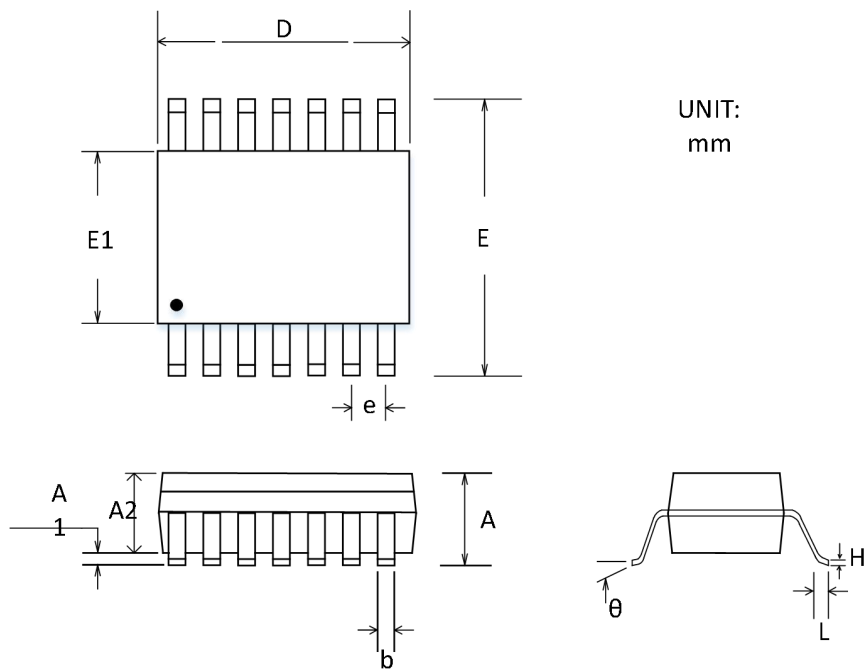
Symbol	Dimensions In Millimeters	
	Min	Max
A	0.820	1.100
A1	0.000	0.150
A2	0.750	0.950
b	0.220	0.380
D	2.800	3.200
E	2.800	3.200
E1	4.650	5.150
e	0.650 BSC	
L	0.400	0.800
θ	0°	8°

SOP-8



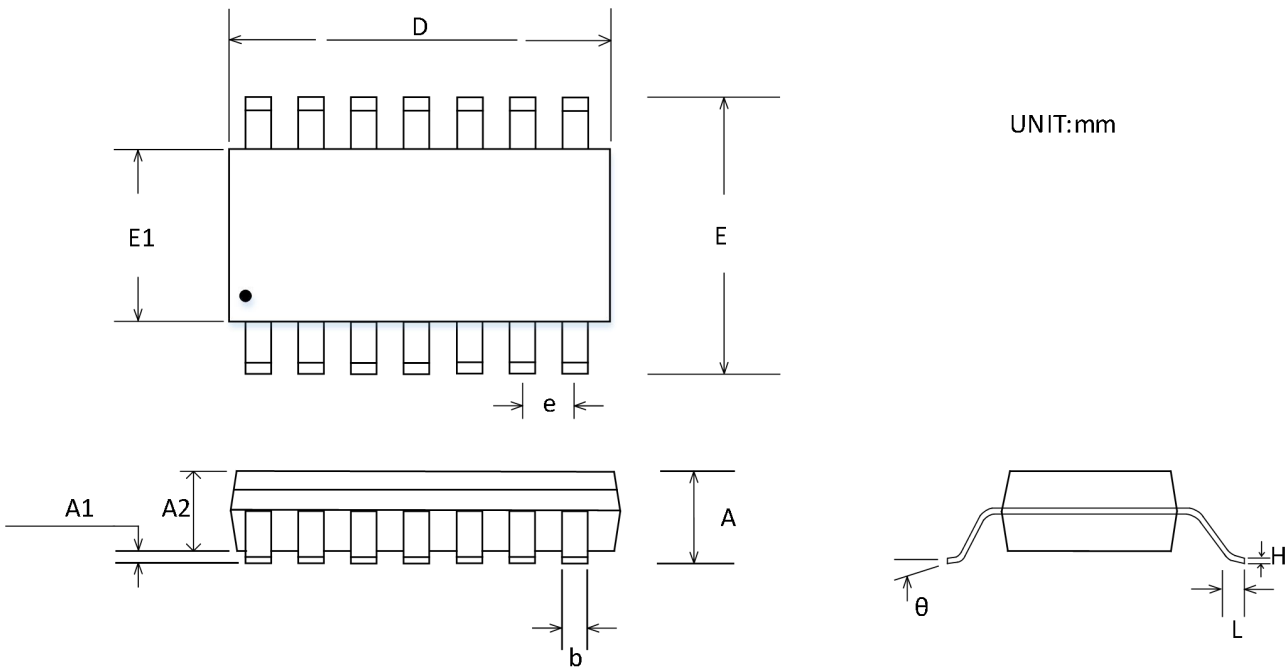
Symbol	Dimensions In Millimeters	
	Min	Max
A	1.350	1.750
A1	0.100	0.250
A2	1.350	1.550
b	0.310	0.510
D	4.800	5.000
E	3.800	4.000
E1	5.800	6.200
e	1.270 BSC	
L	0.400	1.270
θ	0°	8°

TSSOP-14



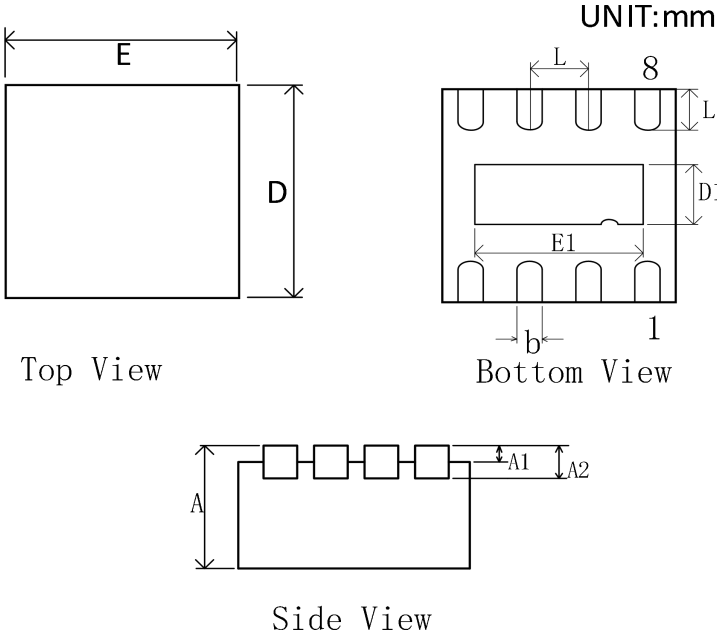
Symbol	Dimensions In Millimeters	
	Min	Max
A	1.20MAX	
A1	0.05	0.15
A2	0.80	1.05
b	0.19	0.30
D	4.90	5.10
E	6.40BSC	
E1	4.30	4.50
e	0.65BSC	
H	0.09	0.20
L	0.45	0.75
θ	0°	8°

SOP-14



Symbol	Dimensions In Millimeters	
	Min	Max
A	1.35	1.75
A1	0.10	0.25
A2	1.25	1.50
b	0.31	0.51
D	8.55	8.75
E	5.80	6.20
E1	3.80	4.00
e	1.27BSC	
H	0.17	0.25
L	0.40	1.27
θ	0°	8°

DFN-8



Symbol	Dimensions In Millimeters			Dimensions Inches		
	Min	Nom	Max	Min	Nom	Max
A	0.80	0.85	0.9	0.031	0.033	0.035
A1	0.00	0.02	0.05	0.000	0.001	0.002
A2	0.153	0.203	0.253	0.006	0.008	0.010
b	0.18	0.24	0.30	0.007	0.009	0.012
D	1.9	2.0	2.1	0.075	0.079	0.083
E	1.9	2.0	2.1	0.075	0.079	0.083
D1	0.5	0.6	0.7	0.020	0.024	0.028
E1	1.1	1.2	1.3	0.043	0.047	0.051
e		0.50			0.20	
k	0.2			0.008		
L	0.25	0.35	0.45	0.010	0.014	0.018
A	0.80	0.85	0.9	0.031	0.033	0.035

Package/Ordering Information

MODEL	TEMPERATURE	PACKAGE DESCRIPTION	PACKAGE OPTION
CD6041AST5	-40°C~125°C	SOT23-5	Tape and Reel, 3000
CD6041AS8	-40°C~125°C	SOP-8	Tape and Reel, 2500
CD6041AS8-RL	-40°C~125°C	SOP-8	Tape and Reel, 3000
CD6041AS8-REEL	-40°C~125°C	SOP-8	Tape and Reel, 4000
CD6042AS8	-40°C~125°C	SOP-8	Tape and Reel, 2500
CD6042AS8-RL	-40°C~125°C	SOP-8	Tape and Reel, 3000
CD6042AS8-REEL	-40°C~125°C	SOP-8	Tape and Reel, 4000
CD6042AMS8	-40°C~125°C	MSOP-8	Tape and Reel, 3000
CD6044AS14	-40°C~125°C	SOP-14	Tape and Reel, 2500
CD6044AS14-RL	-40°C~125°C	SOP-14	Tape and Reel, 3000
CD6044AS14-REEL	-40°C~125°C	SOP-14	Tape and Reel, 4000
CD6044ATS14	-40°C~125°C	TSSOP-14	Tape and Reel, 2500
CD6044ATS14-RL	-40°C~125°C	TSSOP-14	Tape and Reel, 3000
CD6044ATS14-REEL	-40°C~125°C	TSSOP-14	Tape and Reel, 4000

Revision Log

Version	Revision date	Change content	Reason for Change	Modified by	Reviewed By	Note
V1.0	2025.5.26	Initial version	Regular update	WW	LYL	