



CDG719

2:1 Mux/SPDT Switch

Version: Rev 1.0.0 Date: 2025-6-18

Features ■■

- 1.8 V to 5.5 V single supply
- 2.5 Ω (typ) on resistance
- 0.75 Ω (typ) on resistance flatness
- -3 dB bandwidth > 200 MHz
- Rail-to-rail operation
- 8-Lead SOP package
- Fast switching times: $t_{ON} = 12\text{ ns}$ $t_{OFF} = 6\text{ ns}$
- Typical power consumption: (< 0.01 μW)
- TTL/CMOS compatible

Application ■■

- Battery-powered systems
- Communication systems
- Sample-and-hold systems
- Audio signal routing
- Video switching
- Mechanical reed relay replacement

Description ■■

The CDG719 is a monolithic CMOS SPDT switch. This switch provides low power dissipation yet gives high switching speed, low on resistance, and low leakage currents. The CDG719 can operate from a single-supply range of 1.8 V to 5.5 V.

Each switch of the CDG719 conducts equally well in both directions when on. The CDG719 exhibits break-before-make switching action. Because of the advanced submicron process, -3 dB bandwidths of greater than 200 MHz can be achieved.

The CDG719 is available in an 8-lead MSOP package and SOT23-6 package.

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Pin Configurations

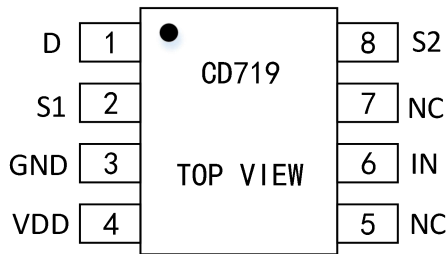


Figure 1. MSOP8 Pin Configuration

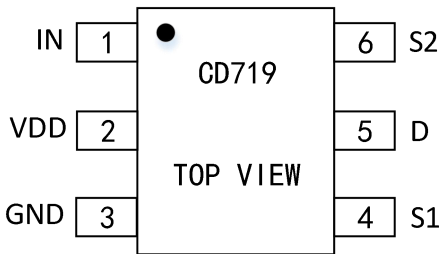


Figure 2. SOT23-6 Pin Configuration

Pin Description

Pin No.		Pin Name	Description
MSOP	SOT-23		
1	5	D	Drain Terminal. Can be used as an input or output.
2	4	S1	Source Terminal. Can be used as an input or output.
3	3	GND	Ground 0V.
4	2	VDD	Positive Power Supply.
5	-	NC	Not Internally Connected.
6	1	IN	Digital Switch Control.
7	-	NC	Not Internally Connected.
8	6	S2	Source Terminal. Can be used as an input or output.

Functional Block diagram

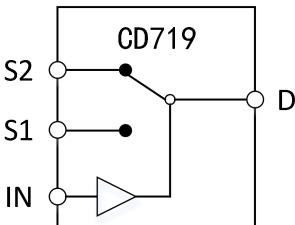


Figure 3. Block Diagram

Truth Table

IN	S1	S2
0	ON	OFF
1	OFF	ON

Absolute Maximum Ratings

Parameter	Rating
V_{DD} to GND	-0.3 V to +7 V
Analog, Digital Inputs ¹	-0.3 V to $V_{DD} + 0.3$ V or 30 mA, whichever occurs first
Peak Current, S or D	100 mA
Continuous Current, S or D	30mA
Operating Temperature Range	-40°C to +125°C
Storage Temperature Range	-65°C to +150°C
Junction Temperature	150°C
MSOP Package, Power Dissipation	315 mW
θ_{JA} Thermal Impedance	206°C/W
θ_{JC} Thermal Impedance	44°C/W
SOT-23 Package, Power Dissipation	282 mW
θ_{JA} Thermal Impedance	229.6°C/W
θ_{JC} Thermal Impedance	91.99°C/W
Lead Temperature, Soldering (10 sec)	300°C
IR Reflow, Peak Temperature (<20sec)	220°C
Soldering (Pb-Free) Reflow, Peak Temperature	260(+0/-5)°C
Time at Peak Temperature	20 sec to 40 sec
ESD	1 kV

Electrical Characteristics

($V_{DD} = 5\text{ V} \pm 10\%$, $GND = 0\text{ V}$, $T_A = 25^\circ\text{C}$.)

PARAMETER	CONDITION	CDG719			
		MIN	TYP	MAX	UNIT
Analog Switch					
Analog Signal Range					V
On Resistance (R _{ON})	V _S = 0 V to V _{DD} , I _S = −10 mA; See Figure 7	--	2.5	7	Ω
On Resistance Match Between Channels (ΔR _{ON})	V _S = 0 V to V _{DD} , I _S = −10 mA	--	0.1	0.4	Ω
On Resistance Flatness (RFLAT(ON))	V _S = 0 V to V _{DD} , I _S = −10 mA	--	0.75	1.5	Ω
Leakage Currents					
Source Off Leakage I _S (Off)	V _{DD} = 5.5 V ;V _S = 4.5V/1 V, V _D = 1 V/4.5 V;See Figure 9	--	± 0.01	± 0.25	nA
Channel On Leakage I _D , I _S (On)	V _S = V _D = 1 V or V _S = V _D = 4.5 V;;See Figure 8	--	± 0.01	± 0.25	nA
Digital Inputs					
Input High Voltage, V _{INH}		2.4	--	--	V
Input Low Voltage, V _{INL}		--	--	0.8	V
Input Current I _{INL} or I _{INH}	V _{IN} = V _{INL} or V _{INH}	--	0.00 5	--	μA
Dynamic Characteristics					
t _{ON}	R _L = 300 Ω, C _L = 35 pF, V _S = 3 V;	--	7	12	ns
t _{OFF}	R _L = 300 Ω, C _L = 35 pF, V _S = 3 V;	--	3	6	ns
Break-Before-Make Time Delay, t _D	R _L = 300 Ω, C _L = 35 pF, V _{S1} = V _{S2} = 3V;	1	8	--	ns
Off Isolation	R _L = 50 Ω, C _L = 5 pF, f = 10 MHz	--	-67	--	dB
	R _L = 50 Ω, C _L = 5 pF, f = 1 MHz	--	-87	--	dB
Channel-to-Channel Crosstalk	R _L = 50 Ω, C _L = 5 pF, f = 10 MHz	--	-62	--	dB
	R _L = 50 Ω, C _L = 5 pF, f = 1 MHz	--	-68	--	dB
Bandwidth −3 dB	R _L = 50 Ω, C _L = 5 pF;	--	200	--	MHz
C _S (Off)		--	7	--	pF
C _D , C _S (On)		--	27	--	pF
Power Requirements					
I _{DD}	V _{DD} = 5.5 V ,Digital inputs = 0 V or 5.5 V	--	0.00 1	1.0	μA

($V_{DD} = 3\text{ V} \pm 10\%$, $GND = 0\text{ V}$, $T_A = 25^\circ\text{C}$.)

PARAMETER	COND ITION	CDG719			
		MIN	TYP	MAX	UNIT
Analog Switch					
Analog Signal Range					V
On Resistance (R _{ON})	V _S = 0 V to V _{DD} , I _S = -10 mA;See Figure 7	--	6	12	Ω
On Resistance Match Between Channels (ΔR _{ON})	V _S = 0 V to V _{DD} , I _S = -10 mA	--	0.1	0.4	Ω
On Resistance Flatness (RFLAT(ON))	V _S = 0 V to V _{DD} , I _S = -10 mA	--	2.5	--	Ω
Leakage Currents					
Source Off Leakage I _S (Off)	V _{DD} = 3.3 V ;V _S = 3 V/1 V, V _D = 1 V/3 V;See Figure 9	--	± 0.01	± 0.25	nA
Channel On Leakage I _D , I _S (On)	V _S = V _D = 1 V or V _S = V _D = 3 V;;See Figure 8	--	± 0.01	± 0.25	nA
Digital Inputs					
Input High Voltage, V _{INH}		2.0	--	--	V
Input Low Voltage, V _{INL}		--	--	0.8	V
Input Current I _{INL} or I _{INH}	V _{IN} = V _{INL} or V _{INH}	--	0.00 5	--	μA
Dynamic Characteristics					
t _{ON}	R _L = 300 Ω, C _L = 35 pF, V _S = 3 V;	--	7	12	ns
t _{OFF}	R _L = 300 Ω, C _L = 35 pF, V _S = 3 V;	--	3	6	ns
Break-Before-Make Time Delay, t _D	R _L = 300 Ω, C _L = 35 pF, V _{S1} = V _{S2} = 3V;	1	8	--	ns
Off Isolation	R _L = 50 Ω, C _L = 5 pF, f = 10 MHz	--	-67	--	dB
	R _L = 50 Ω, C _L = 5 pF, f = 1 MHz	--	-87	--	dB
Channel-to-Channel Crosstalk	R _L = 50 Ω, C _L = 5 pF, f = 10 MHz	--	-62	--	dB
	R _L = 50 Ω, C _L = 5 pF, f = 1 MHz	--	-82	--	dB
Bandwidth -3 dB	R _L = 50 Ω, C _L = 5 pF;	--	200	--	MHz
C _S (Off)		--	7	--	pF
C _D , C _S (On)		--	27	--	pF
Power Requirements					
I _{DD}	V _{DD} = 3.3 V ,Digital inputs = 0 V or 3.3 V	--	0.00 1	1.0	μA

Typical Characteristics

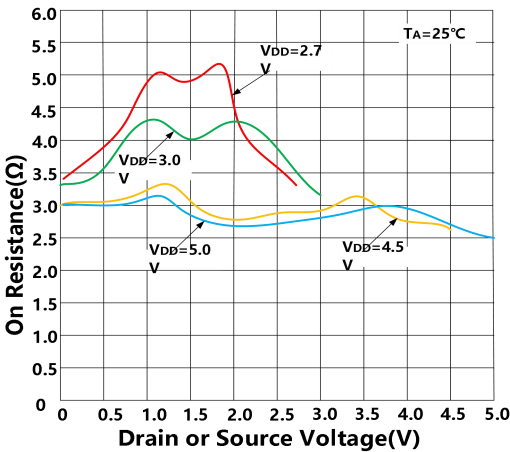


Figure 4. On Resistance vs. V_D (V_S), Single Supplies

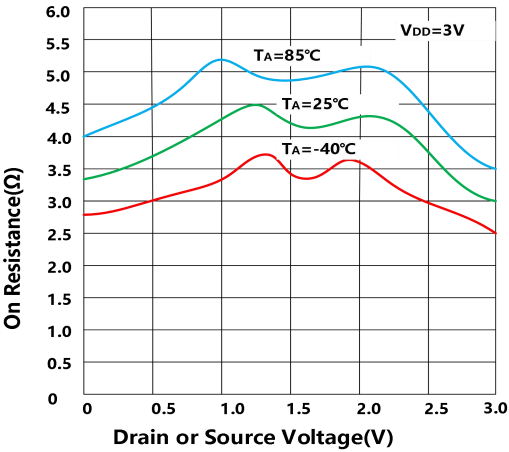


Figure 5. On Resistance vs. V_D (V_S) for Different Temperatures, $V_{DD} = 3V$

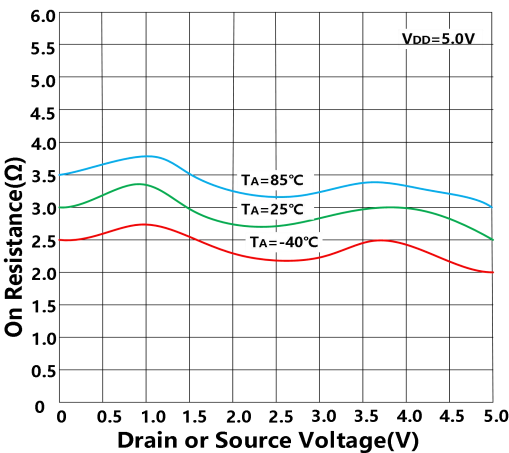


Figure 6. On Resistance vs. V_D (V_S) for Different Temperatures, $V_{DD} = 5V$

Test Circuit

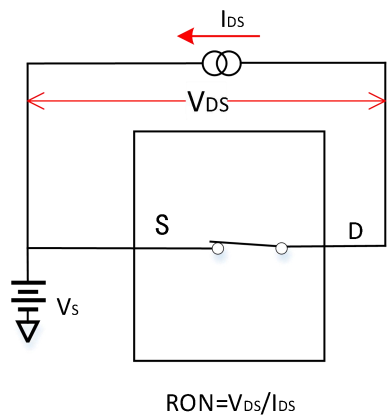


Figure 7. On Resistance

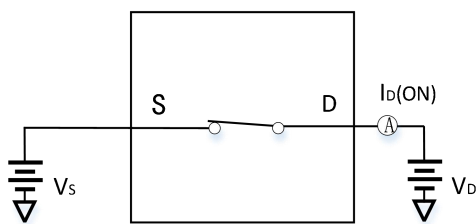


Figure 8. On Leakage

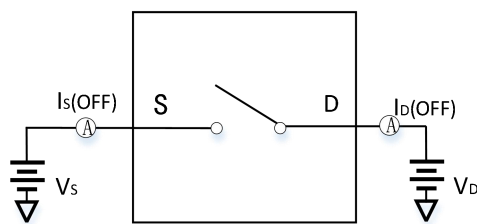


Figure 9. Off Leakage

Package Outline Dimensions

MSOP-8

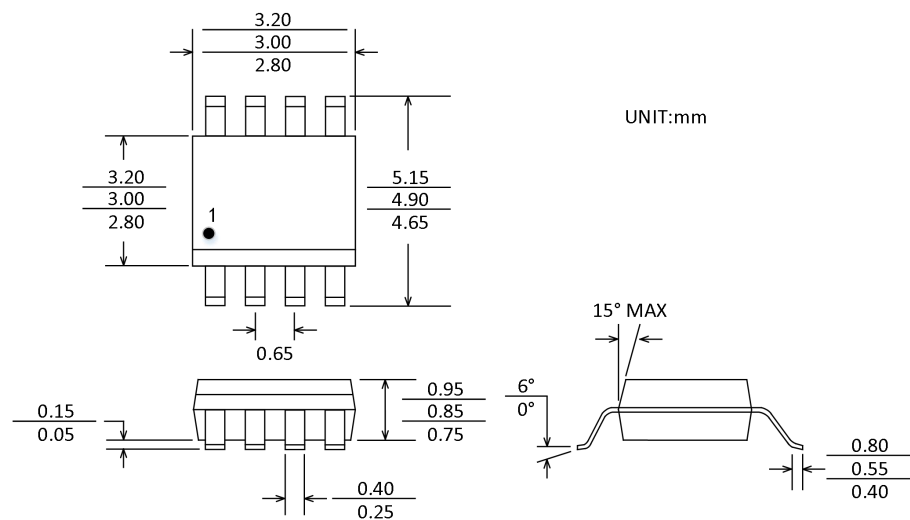


Figure 10. MSOP8 Outline Package

SOT23-6

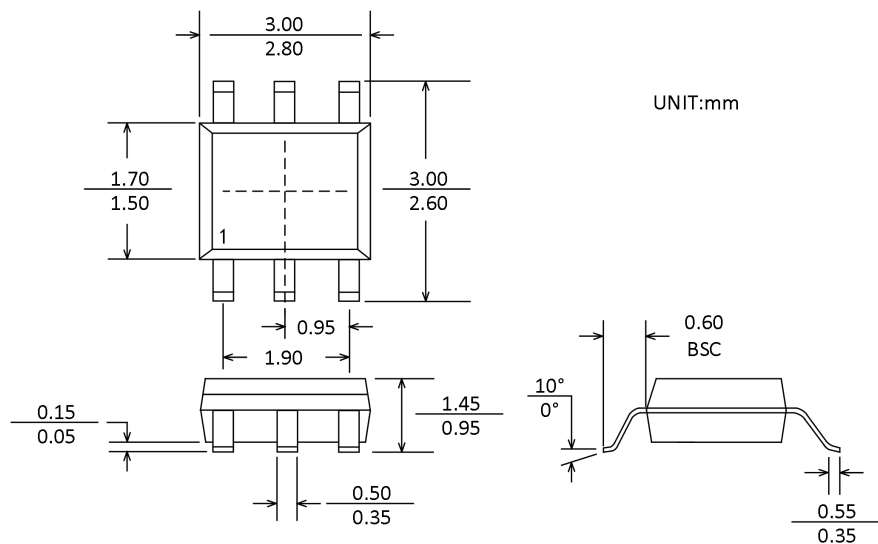


Figure 11. SOT23-6 Outline Package

Package/Ordering Information

MODEL	TEMPERATURE	PACKAGE DESCRIPTION	PACKAGE OPTION
CDG719AMS8	-40°C~125°C	MSOP-8	Tape and Reel, 2500
CDG719AMS8-RL	-40°C~125°C	MSOP-8	Tape and Reel, 3000
CDG719AMS8-REEL	-40°C~125°C	MSOP-8	Tape and Reel, 4000
CDG719AST6	-40°C~125°C	SOT23-6	Tape and Reel, 3000

Revision Log

Version	Revision date	Change content	Reason for Change	Modified by	Reviewed By	Note
V1.0	2025.6.18	Initial version	Regular update	WW	LYL	