



CD317

Low Dropout Linear Regulator (LDO)

Version: Rev 1.0.0 Date: 2025-6-12

Features ■■

- Dropout Voltage: 1.39V(Typ)
- Reference/Output Voltage Trimmed to $\pm 2\%$
- Maximum Input Voltage: 15V
- Adjustable Output Voltage or Fixed: ADJ, 1.5V,
- Load Regulation typically at 0.4%(Max)
- Current Limiting and Thermal Protection
- Standard 3-Pin Power Packages
- Operating Temperature Range: -40 to +125°C

Application ■■

- Post Regulator for switching DC/DC Converter
- High Efficiency Linear Regulator
- Battery Chargers
- PC Add on Card
- Motherboard clock supplies
- LCD Monitor
- Set-top Box

Description ■■

The CD1117 is a series of low dropout voltage regulators which can provide up to 0.8A of output current. The CD1117 is available in fixed voltage, 1.5, 1.8, 2.5, 3.3 and 5.0V. Additionally it is also available in adjustable version. On chip precision trimming adjusts the reference/ output voltage to within $\pm 2\%$. Current limit is also trimmed to ensure specified output current and controlled short-circuit current.

The CD1117 series is available in SOT-223, TO-252 packages. A minimum of 10uF tantalum capacitor is required at the output to improve the transient response and Stability.

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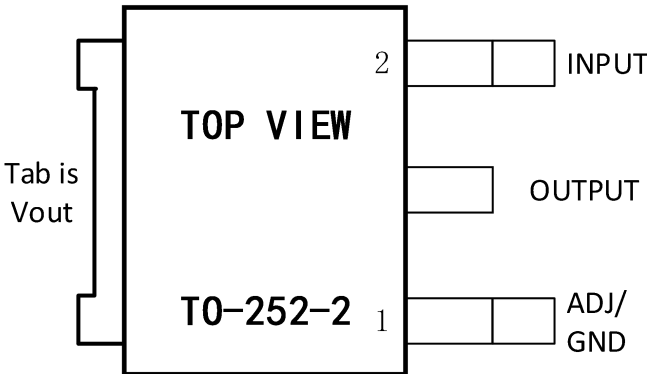
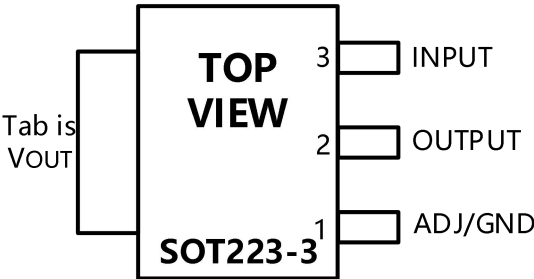
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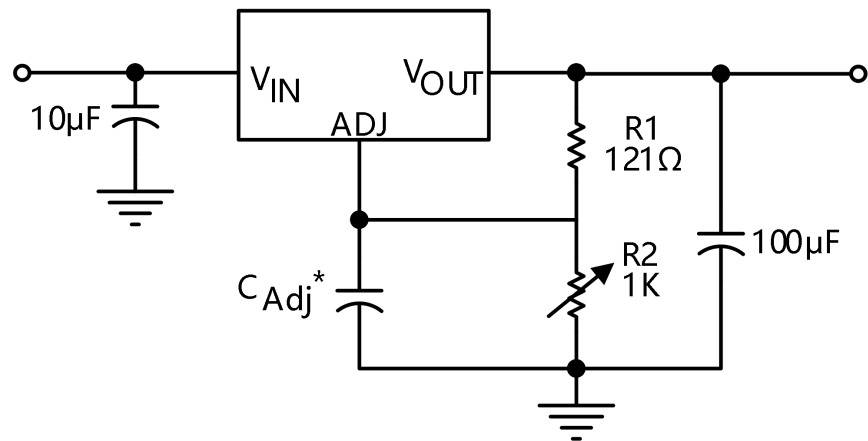
Pin Configurations



Pin Description

Num	Symbol	I/O	Description
1	ADJ/GND	--	Adjust pin for adjustable output option. Ground pin for fixed output option
2	OUTPUT	O	Output voltage pin for the regulator
3	INPUT	I	Input voltage pin for the regulator

Application Circuit



$$V_{OUT} = 1.25 \left(1 + \frac{R2}{R1} \right)$$

* C_{ADJ} is optional,however it will improve ripple rejection.

Absolute Maximum Ratings

Rating	Symbol	Value	Unit
V _{IN}	Input Voltage	15	V
I _{OUT}	DC Output Current	PD/(V _{IN} -V _O)	mA
T _J	Operating Junction Temperature Range (* in case of CD1117X-(1.2)	-40 to 125 (* 0 to 150)	°C
θ _{JA}	Thermal Resistance Junction-to-Ambient (SOT-223)	150	°C/W
θ _{JA}	Thermal Resistance Junction-to-Ambient (TO-252)	80	°C/W
θ _{JA}	Thermal Resistance Junction-to-Ambient (TO-220)	60	°C/W
θ _{JC}	Thermal Resistance Junction-to-Case (SOT-223)	35	°C/W
θ _{JC}	Thermal Resistance Junction-to-Case (TO-252)	22	°C/W
θ _{JC}	Thermal Resistance Junction-to-Case (TO-220)	15	°C/W

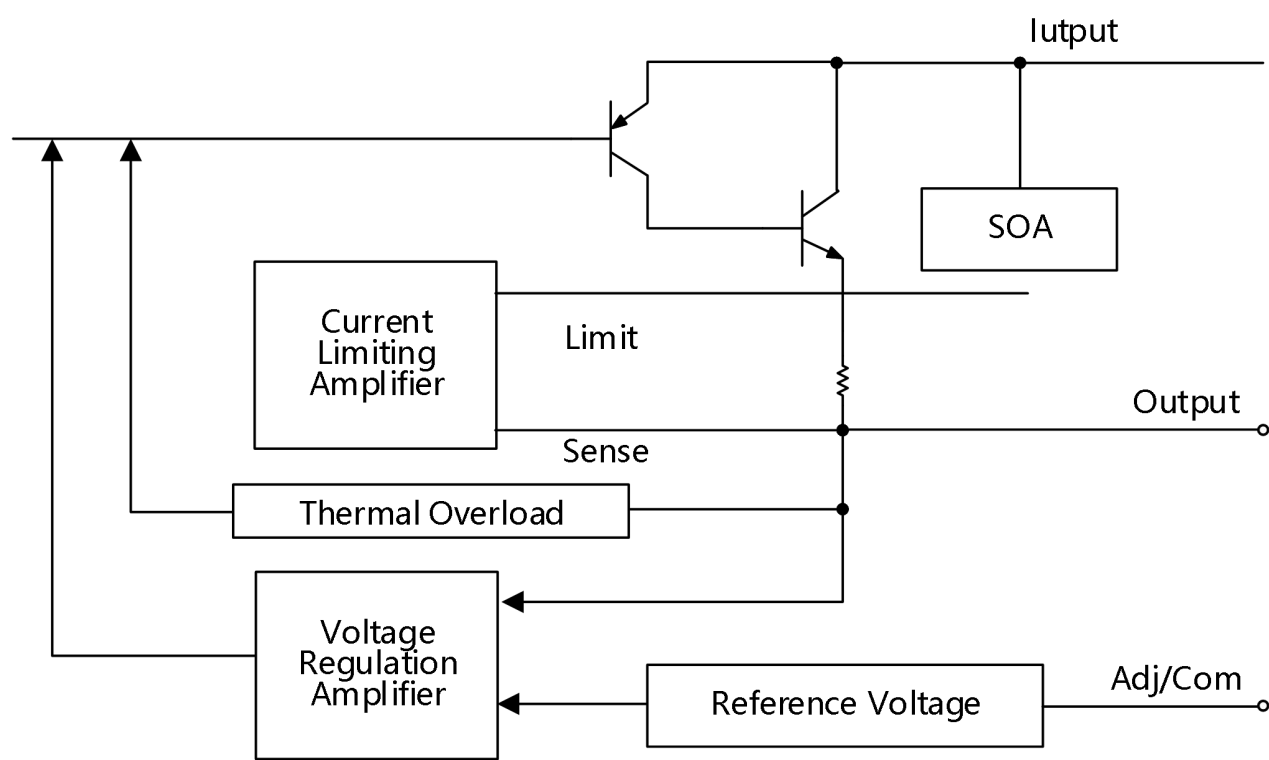
Electrical characteristics

$T_J = 25^\circ\text{C}$, unless otherwise specified

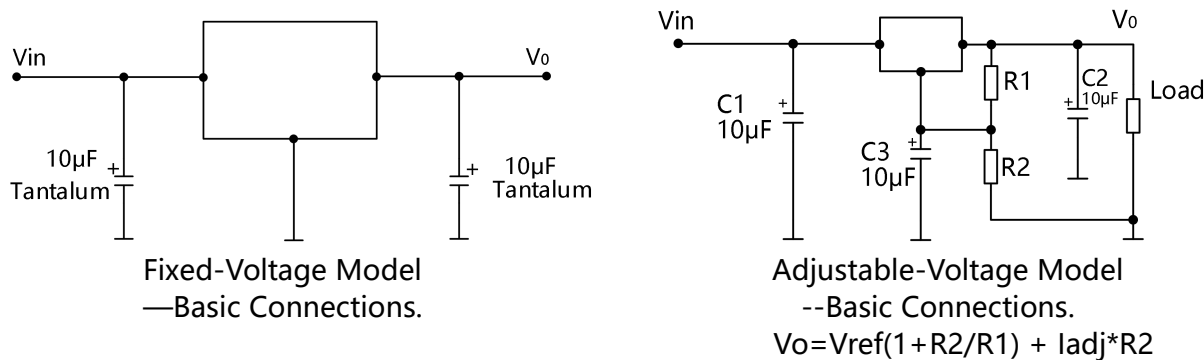
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
OUTPUT VOLTAGE					
CD1117-1.25 (Adjustable)	I _o = 0A to 0.8A, V _{IN} = 2.5 to 10V	1.225	1.250	1.280	V
CD1117-1.5	I _o = 0A to 0.8A, V _{IN} = 3.0 to 10V	1.470	1.500	1.530	
CD1117-1.8	I _o = 0A to 0.8A, V _{IN} = 3.2 to 10V	1.764	1.800	1.836	
CD1117-2.5	I _o = 0A to 0.8A, V _{IN} = 3.9 to 10V	2.450	2.500	2.550	
CD1117-3.3	I _o = 0A to 0.8A, V _{IN} = 4.75 to 10V	3.240	3.300	3.360	
CD1117-5.0	I _o = 0A to 0.8A, V _{IN} = 6.5 to 12V	4.900	5.000	5.100	
LINE REGULATION					
CD1117-1.25 (Adjustable)	V _{IN1} -V _O =1.5V; V _{IN2} -V _O =13.75V, I _O =10mA, V _O =5V	--	--	2	%
CD1117-1.5	I _o = 0A, V _{IN} = 2.8 to 10V	--	23	40	mV
CD1117-1.8	I _o = 0A, V _{IN} = 3.2 to 10V	--	25	40	mV
CD1117-2.5	I _o = 0A, V _{IN} = 3.9 to 10V	--	30	45	mV
CD1117-3.3	I _o = 0A, V _{IN} = 4.75 to 15V	--	32	45	mV
CD1117-5.0	I _o = 0A, V _{IN} = 6.5 to 15V	--	48	55	mV
LOAD REGULATION					
CD1117-1.25 (Adjustable)	I _o = 0A to 0.8A, V _{IN} -V _{OUT} = 3.0V	--	0.2	0.4	%
CD1117-1.5	I _o = 0A to 0.8A, V _{IN} = 3.0V	--	10	25	mV
CD1117-1.8	I _o = 0A to 0.8A, V _{IN} = 3.2V	--	12	25	mV
CD1117-2.5	I _o = 0A to 0.8A, V _{IN} = 3.9V	--	13	25	mV
CD1117-3.3	I _o = 0A to 0.8A, V _{IN} = 4.75V	--	14	25	mV
CD1117-5.0	I _o = 0A to 0.8A, V _{IN} = 6.5V	--	17	25	mV
DROPOUT VOLTAGE					
All Models Dropout Voltage	Vin=6.5V, I _O =100mA (T _j = 25°C)	--	1.27	1.35	V
	Vin=6.5V, I _O =500mA (T _j = 25°C)	--	1.34	1.55	V
	Vin=6.5V, I _O =800mA (T _j = 25°C)	--	1.39	1.60	V
Current Limit	V _{IN} - V _O = 5V (T _j = 25°C)	800	1000	1200	mA
Minimum Load Current Adjustable Models	V _{IN} = 13.75V	--	--	5	mA

Quiescent Current	$V_{IN} = 12V$	--	5	10	mA
Adjust Pin Current vs Load Current, CD1117	$I_O = 10mA, V_{IN} - V_O = 1.4 \text{ to } 10V$	--	50	120	μA
	$I_O = 10mA \text{ to } 0.8A, V_{IN} - V_O = 1.4 \text{ to } 10V$	--	0.5	5	μA
Temperature Drift	$T_J = -40^{\circ}C \text{ to } +125^{\circ}C$	--	0.5	--	%
RMS Output Noise	Bandwidth of 10Hz to 10kHz at 25°C	--	0.003	--	% V_O
Ripple Rejection Ratio	120Hz input Ripple(C_{ADJ} for ADJ) = 25 μF $V_{IN} - V_O = 5V, I_O = 100mA, T_J = -40^{\circ}C \text{ to } +125^{\circ}C$	40	60	--	dB

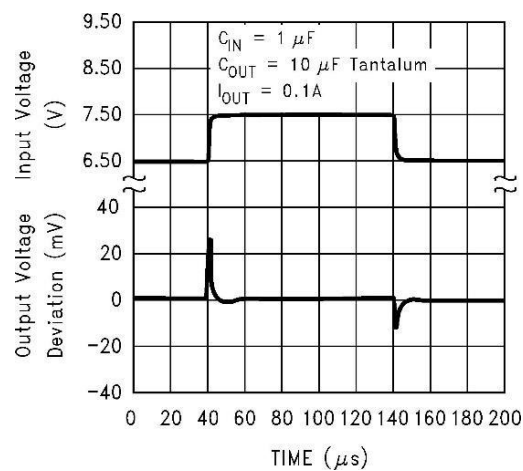
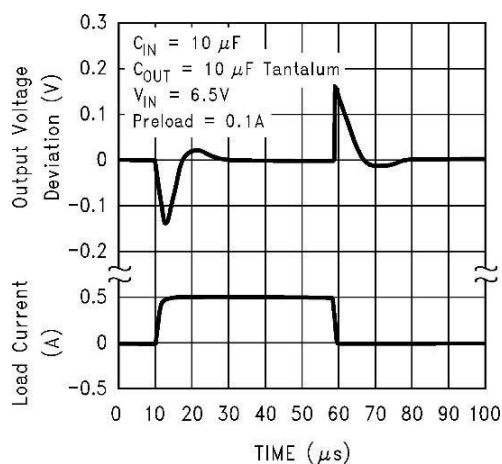
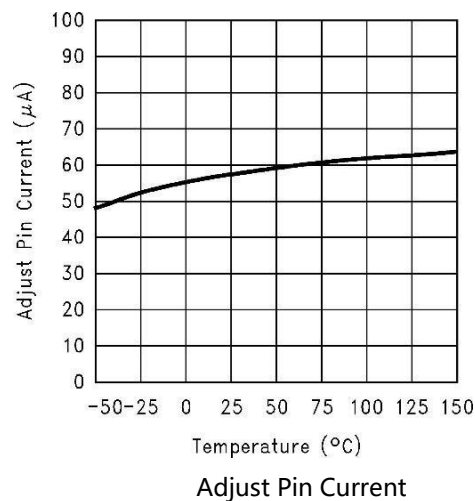
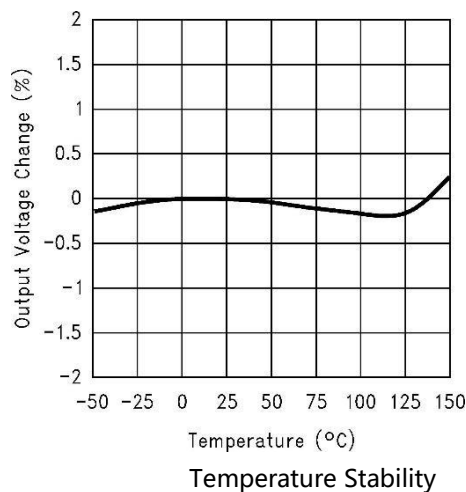
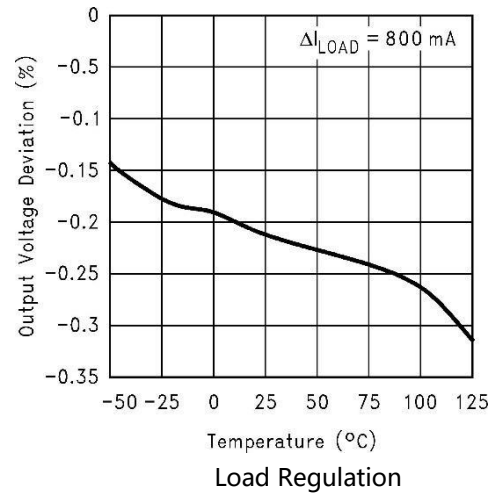
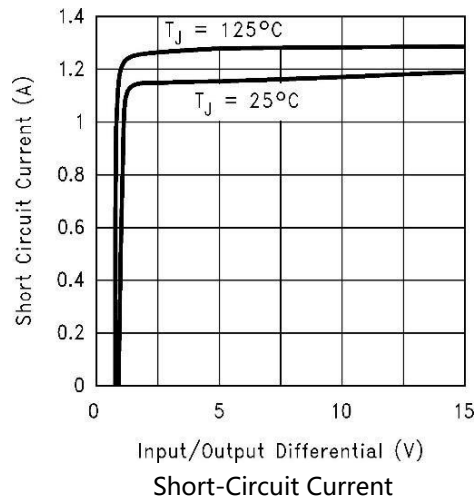
Block Diagram



Application Diagram



Typical Characteristics



Application Information

Output voltage adjustment

Like most regulators, the CD1117 regulates the output by comparing the output voltage to an internally generated reference voltage. On the adjustable version as shown in Fig.4, the V_{REF} is available externally as 1.25V between V_{OUT} and ADJ. The voltage ratio formed by R1 and R2 should be set to conduct 10mA (minu-mum output load).

The output voltage is given by the following equation: $V_O = V_{REF}(1 + R2/R1) + I_{ADJ} * R2$

On fixed versions of CD1117, the voltage divider is provided internally

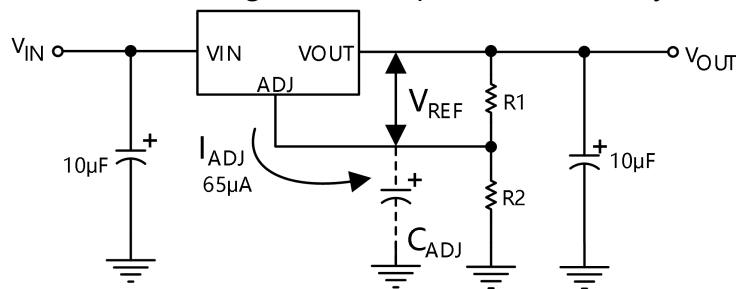


Figure 4. Basic Adjustable Regulator

Input Bypass Capacitor

An input capacitor is recommended. A 10µF tantalum on the input is a suitable input bypassing for almost all applications.

Adjust Terminal Bypass Capacitor

The adjust terminal can be bypassed to ground with a bypass capacitor (C_{ADJ}) to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. At any ripple frequency, the impedance of the C_{ADJ} should be less than R1 to prevent the ripple from being amplified:

$$(2\pi * f_{RIPPLE} * C_{ADJ}) < R1$$

The R1 is the resistor between the output and the adjust pin. Its value is normally in the range of 100- 200Ω. For example, with $R1 = 124\Omega$ and $f_{RIPPLE} = 120\text{Hz}$, the C_{ADJ} should be 11µF.

Output Capacitor

CD1117 requires a capacitor from V_{OUT} to GND to provide compensation feedback to the internal gain stage. This is to ensure stability at the output terminal. Typically, a 10µF tantalum or 50µF aluminum electrolytic is sufficient.

Note: The ESR is typically 1.0 Ω.

The output capacitor does not have a theoretical upper limit and increasing its value will increase stability. $C_{OUT} = 100\mu\text{F}$ or more is typical for high current regulator design.

Load Regulation

When the adjustable regulator is used (Fig.5), the best load regulation is accomplished when the

top of the resistor divider (R1) is connected directly to the output pin of the CD1117. When so connected, R_P is not multiplied by the divider ratio. For Fixed output version, the top of R1 is internally connected to the output and ground pins can be connected to low side of the load.

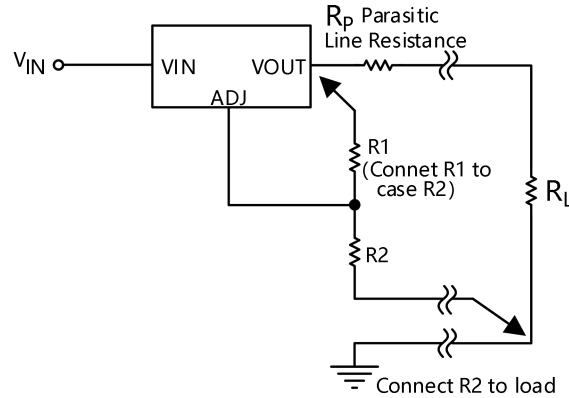


Figure 5. Best Load Regulation Using Adjustable Output Regulator

Thermal Protection

CD1117 has thermal protection which limits junction temperature to 150°C. However, device functionality is only guaranteed to a maximum junction temperature of +125°C. The power dissipation and junction temperature for CD1117 in DPAK package are given by

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT}$$

$$T_{JUNCTION} = T_{AMBIENT} + (P_D \times \theta_{JA})$$

Note: $T_{JUNCTION}$ must not exceed 125°C

Current Limit Protection

CD1117 is protected against overload conditions. Current protection is triggered at typically 1.6A.

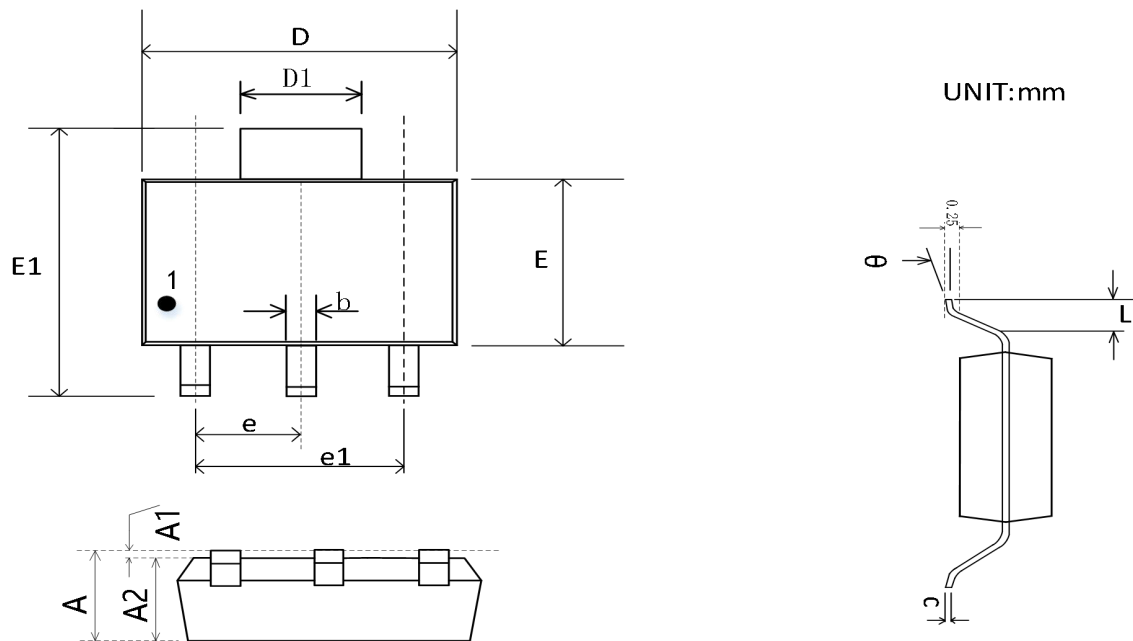
Thermal Consideration

The CD1117 series contain thermal limiting circuitry designed to protect itself from over-temperature conditions. Even for normal load conditions, maximum junction temperature ratings must not be exceeded. As mention in thermal protection section, we need to consider all sources of thermal resistance between junction and ambient. It includes junction-to-case, case-to-heat-sink interface, and heat sink thermal resistance itself.

Junction-to-case thermal resistance is specified from the IC junction to the bottom of the case directly below the die. Proper mounting is required to ensure the best possible thermal flow from this area of the package to the heat sink. The case of all devices in this series is electrically connected to the output. Therefore, if the case of the device must be electrically isolated, a thermally conductive spacer is recommended.

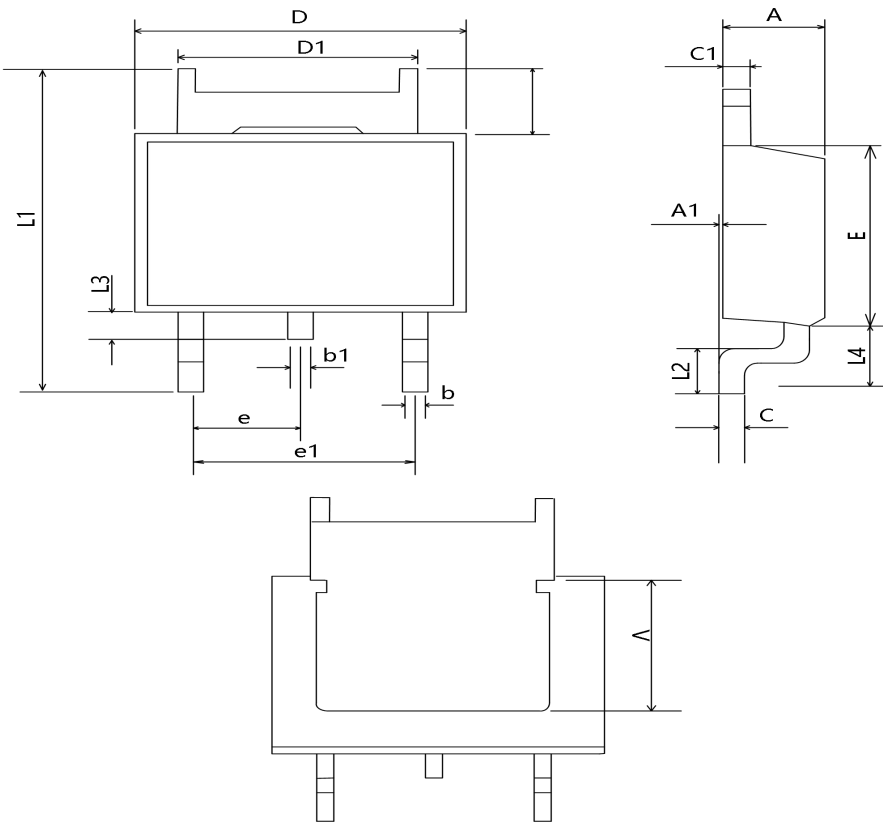
Package Outline Dimensions

SOT-223-3



SYMBOL	DIMENSIONS IN MILLIMETERS		DIMENSIONS IN INCHES	
	MIN	MAX	MIN	MAX
A	1.520	1.800	0.060	0.071
A1	0.02	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.840	0.026	0.033
c	0.240	0.320	0.010	0.014
D	6.300	6.700	0.248	0.264
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.700	7.300	0.264	0.287
e	2.300 BSC		0.091 BSC	
e1	4.500	4.700	0.177	0.185
L	0.750	--	0.030	--
θ	0°	10°	0°	10°

T0252-2



SYMBOL	DIMENSIONS IN MILLIMETERS		DIMENSIONS IN INCHES	
	MIN	MAX	MIN	MAX
A	--	2.550	--	0.100
A1	0.000	0.127	0.000	0.005
b	0.640	0.880	0.025	0.035
b1	0.700	0.900	0.028	0.035
C	0.460	0.600	0.018	0.024
C1	0.460	0.880	0.018	0.035
D	6.350	6.730	0.250	0.265
D1	4.960	5.460	0.195	0.215
E	5.970	6.220	0.235	0.245
e	2.285 TYP		0.090TYP	
e1	4.500	4.700	0.177	0.185
L1	9.400	10.42	0.370	0.041

L2	1.400	1.780	0.055	0.070
L3	0.640	1.020	0.025	0.040
L4	2.550	2.900	0.100	0.114
V	4.320	--	0.170	--

Package/Ordering Information

MODEL	TEMPERATURE	PACKAGE DESCRIPTION	PACKAGE OPTION
CD1117C-1.25(ADJ)	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-1.25(ADJ)	-40°C~125°C	TO252-2	Tape and Reel,2500
CD1117C-1.5	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-1.5	-40°C~125°C	TO252-2	Tape and Reel,2500
CD1117C-1.8	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-1.8	-40°C~125°C	TO252-2	Tape and Reel,2500
CD1117C-2.5	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-2.5	-40°C~125°C	TO252-2	Tape and Reel,2500
CD1117C-3.3	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-3.3	-40°C~125°C	TO252-2	Tape and Reel,2500
CD1117C-5.0	-40°C~125°C	SOT223-3	Tape and Reel,2500
CD1117DT-5.0	-40°C~125°C	TO252-2	Tape and Reel,2500

Revision Log

Version	Revision date	Change content	Reason for Change	Modified by	Reviewed By	Note
V1.0	2025.6.12	Initial version	Regular update	WW	LYL	