



# CD2576

DC/DC Buck Regulator

Version: Rev 1.0.0 Date: 2025-6-12

## Features ■■

- 3.3 V, 5.0 V, 12 V, 15 V, and Adjustable Output Versions
- Adjustable Version Output Voltage Range, 1.23 to 37 V  $\pm 4\%$
- Maximum Over Line and Load Conditions
- Guaranteed 3.0 A Output Current
- Wide Input Voltage Range
- Requires Only 4 External Components
- 52 kHz Fixed Frequency Internal Oscillator
- TTL Shutdown Capability, Low Power Standby
- High Efficiency
- Uses Readily Available Standard Inductors
- Thermal Shutdown and Current Limit

## Application ■■

- Simple High-Efficiency Step-Down (Buck) Regulator
- Efficient Pre-Regulator for Linear Regulators
- On-Card Switching Regulators
- Positive to Negative Converter
- Negative Step-Up Converters
- Power Supply for Battery Chargers

## Description ■■

The CD2576 series of regulators are monolithic integrated circuits ideally suited for easy and convenient design of a step-down switching regulator (buck converter). All circuits of this series are capable of driving a 3.0 A load with excellent line and load regulation. These devices are available in fixed output voltages of 3.3 V, 5.0 V, 12 V, 15 V, and an adjustable output version.

These regulators were designed to minimize the number of external components to simplify the power supply design. Standard series of inductors optimized for use with the CD2576 are offered by several different inductor manufacturers. Since the CD2576 converter is a switch-mode power supply, its efficiency is significantly higher in comparison with popular three-terminal linear regulators, especially with higher input voltages. In many cases, the power dissipated is so low that no heatsink is required or its size could be reduced dramatically.

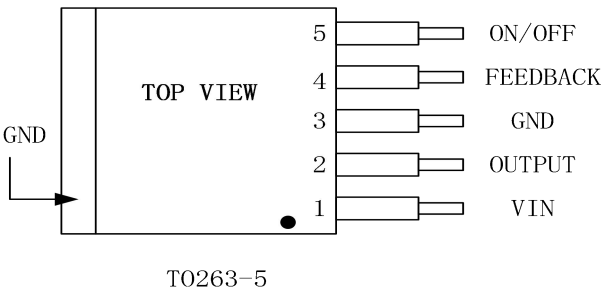
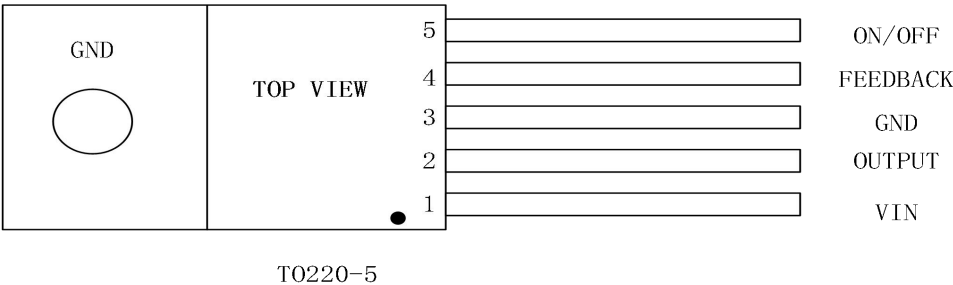
A standard series of inductors optimized for use with the CD2576 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies. The CD2576 features include a guaranteed  $\pm 4\%$  tolerance on output voltage within specified input voltages and output load conditions, and  $\pm 10\%$  on the oscillator frequency ( $\pm 2\%$

over 0°C to 125°C). External shutdown is included, featuring 80  $\mu$ A(typical) standby current. The output switch includes cycle-by-cycle current limiting, as well as thermal shutdown for full protection under fault conditions.

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Pin Configurations



Pin Description

Num	Symbol	I/O	Description
1	VIN	I	Supply input pin to collector pin of high-side transistor. Connect to power supply and input bypass capacitors CIN. Path from VIN pin to high frequency bypass CIN and GND must be as short as possible.
2	OUTPUT	O	Emitter pin of the power transistor. This is a switching node. Attached this pin to an inductor and the cathode of the external diode.
3	GND	--	Ground pin. Path to CIN must be as short as possible.
4	FEEDBACK	I	Feedback sense input pin. Connect to the midpoint of feedback divider to set VOUT for ADJ version or connect this pin directly to the output capacitor for a fixed output version.
5	ON/OFF	I	Enable input to the voltage regulator. High = OFF and low = ON. Connect to GND to enable the voltage regulator. Do not leave this pin float.package.

Typical Application (Fixed Output Voltage Versions)

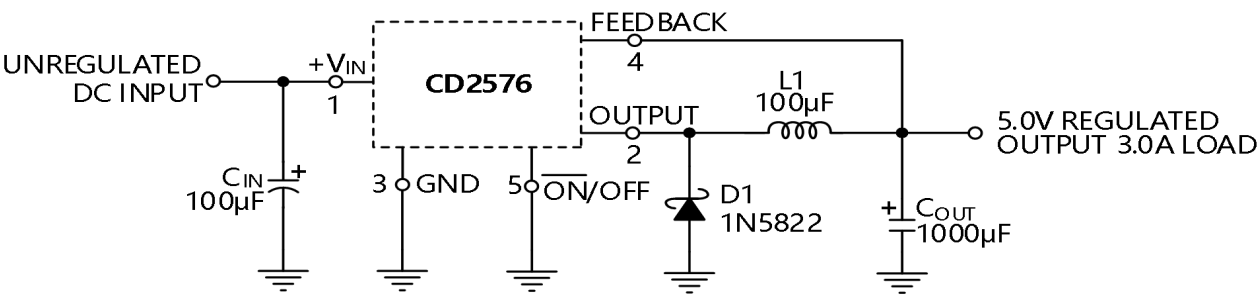


Figure 1.

Representative Block Diagram and Typical Application

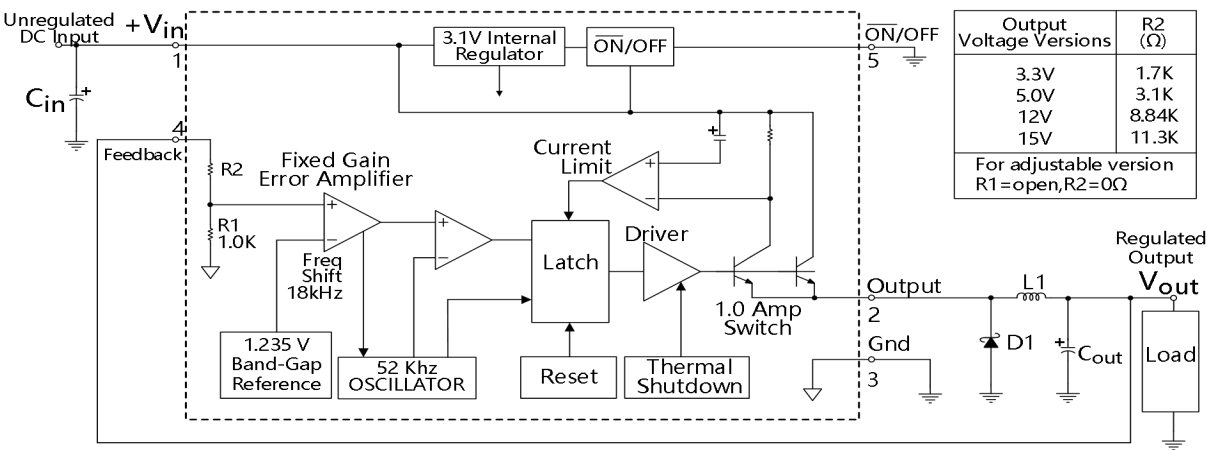


Figure 2.

This device contains 162 active transistors.

Absolute Maximum Ratings

(Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.)

Rating	Symbol	Value	Unit
Maximum Supply Voltage	$V_{IN}$	45	V
ON/OFF Pin Input Voltage		$-0.3V \leq V \leq +V_{IN}$	V
Output Voltage to Ground (Steady-State)		-1.0	V
<b>Power Dissipation</b>			

TO-220,5-Lead	$P_D$	Internally Limited	W
• Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	65	°C/W
• Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5.0	°C/W
T0-263, 5-Lead (D2PAK)	$P_D$	Internally Limited	W
• Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	70	°C/W
• Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5.0	°C/W
Storage Temperature Range	$T_{STG}$	-65°C to +150°C	°C
Minimum ESD Rating (Human Body Model: C=100pF,R=1.5kΩ)		2.0	kV
Lead Temperature (Soldering, 10 seconds)		260	°C
Maximum Junction Temperature	$T_J$	150	°C

\* Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.

Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

## Operating Ratings

(Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.)

Rating	Symbol	Value	Unit
Operating Junction Temperature Range	$T_J$	-40 to +125	°C
Supply Voltage	$V_{IN}$	60	V

## System Parameters [Note 1] (Electrical Characteristics)

(Unless otherwise specified,  $V_{in} = 12\text{ V}$  for the 3.3 V, 5.0 V, and Adjustable version,  $V_{in}=25\text{V}$  for the 12V version, and  $V_{in}=30\text{V}$  for the 15V version.  $I_{Load}=500\text{mA}$ ,  $T_J=25^\circ\text{C}$ , for min/max values  $T_J$  is the operating junction temperature range that applies [Note 2], unless otherwise noted.)

Characteristics	Symbol	Min	Max	Unit
<b>CD2576-3.3</b> ([Note 1] Test Circuit Figure 3)				

Output Voltage	$V_{OUT}$	3.234	3.366	V
Output Voltage ( $6.0V \leq V_{IN} \leq 40V$ , $0.5A \leq I_{Load} \leq 3.0A$ )	$V_{OUT}$			V
$T_J = 25^\circ C$		3.168	3.432	
$T_J = -40$ to $+125^\circ C$		3.135	3.465	
Efficiency ( $V_{IN}=12V$ , $I_{Load} = 3.0A$ )	$\eta$	65	-	%
<b>CD2576-5</b> [Note 1]				
Output Voltage	$V_{OUT}$	4.9	5.1	V
Output Voltage ( $8.0V \leq V_{IN} \leq 40V$ , $0.5A \leq I_{Load} \leq 3.0A$ )	$V_{OUT}$			V
$T_J = 25^\circ C$		4.8	5.2	
$T_J = -40^\circ C$ to $+125^\circ C$		4.75	5.25	
Efficiency ( $V_{IN} = 12V$ , $I_{Load} = 3.0A$ )	$\eta$	67	-	%
<b>CD2576-12</b> [Note 1]				
Output Voltage	$V_{OUT}$	11.76	12.24	V
Output Voltage ( $15.0V \leq V_{IN} \leq 40V$ , $0.5A \leq I_{Load} \leq 3.0 A$ )	$V_{OUT}$			V
$T_J = 25^\circ C$		11.52	12.48	
$T_J = -40^\circ C$ to $+125^\circ C$		11.4	12.6	
Efficiency ( $V_{IN} = 12V$ , $I_{Load} = 3.0A$ )	$\eta$	78	-	%
<b>CD2576-15</b> [Note 1]				
Output Voltage	$V_{OUT}$	14.7	15.3	V
Output Voltage ( $18V \leq V_{IN} \leq 40V$ , $0.5A \leq I_{Load} \leq 3.0A$ )	$V_{OUT}$			V
$T_J = 25^\circ C$		14.4	15.6	
$T_J = -40^\circ C$ to $+125^\circ C$		14.25	15.75	
Efficiency ( $V_{IN} = 12V$ , $I_{Load} = 3.0A$ )	$\eta$	75	-	%
<b>CD2576 ADJUSTABLE VERSION</b> [Note 1]				
Feedback Voltage ( $V_{IN}=12V$ , $I_{Load}=0.5A$ , $V_{OUT}=5.0V$ , $T_J=25^\circ C$ )	$V_{OUT}$	1.217	1.243	V
Feedback Voltage ( $8.0V \leq V_{IN} \leq 40V$ , $0.5A \leq I_{Load} \leq 3.0A$ , $V_{OUT}=5.0 V$ )	$V_{OUT}$			V
$T_J= 25^\circ C$		1.193	1.267	
$T_J = -40^\circ C$ to $+125^\circ C$		1.18	1.28	
Efficiency ( $V_{IN} = 12V$ , $I_{Load}=3.0A$ , $V_{OUT}=5.0V$ )	$\eta$	67	-	%

1.External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance.

When the CD2576 is used as shown in the test circuit, system performance will be as shown in

system parameters section .

2. Tested junction temperature range for the CD2576:  $T_{\text{LOW}} = -40^{\circ}\text{C}$   $T_{\text{HIGH}} = +125^{\circ}\text{C}$

## Device Parameters (Electrical Characteristics)

(Unless otherwise specified,  $V_{\text{IN}} = 12\text{V}$  for the 3.3V, 5.0V, and Adjustable version,  $V_{\text{IN}} = 25\text{V}$  for the 12V version, and  $V_{\text{IN}} = 30\text{V}$  for the 15V version.  $I_{\text{LOAD}} = 500\text{mA}$ ,  $T_J = 25^{\circ}\text{C}$ , for min/max values  $T_J$  is the operating junction temperature range that applies [Note 2], unless otherwise noted.)

Characteristics	Symbol	Min	Max	Unit
<b>ALL OUTPUT VOLTAGE VERSIONS</b>				
Feedback Bias Current ( $V_{\text{OUT}} = 5.0\text{V}$ [Adjustable Version Only])	$I_b$			nA
$T_J = 25^{\circ}\text{C}$		-	100	
$T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		-	500	
Oscillator Frequency [Note 3]	$F_{\text{OSC}}$			kHz
$T_J = 25^{\circ}\text{C}$		-	-	
$T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		42	63	
Saturation Voltage ( $I_{\text{OUT}} = 3.0\text{A}$ [Note 4])	$V_{\text{SAT}}$			V
$T_J = 25^{\circ}\text{C}$		-	1.8	
$T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		-	2.0	
Max Duty Cycle ( "on" ) [Note 5]	$D_C$	93	-	%
Current Limit (Peak Current [Notes 3 and 4])	$I_{\text{CL}}$			A
$T_J = 25^{\circ}\text{C}$		4.2	6.9	
$T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		3.5	7.5	
Output Leakage Current [Notes 6 and 7], $T_J = 25^{\circ}\text{C}$	$I_L$			mA
Output = 0V		-	2.0	
Output = -1.0V		-	30	
Quiescent Current [Note 6]	$I_Q$			mA
$T_J = 25^{\circ}\text{C}$		-	10	
$T_J = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$		-	11	
Standby Quiescent Current (ON/OFF Pin = 5.0 V ( "off" ))	$I_{\text{STBY}}$			uA
$T_J = 25^{\circ}\text{C}$		-	200	
ON/OFF Pin Logic Input Level				V
$V_{\text{OUT}} = 0\text{V}$	$V_{\text{IH}}$			



$T_J = 25^{\circ}\text{C}$		2.2	-	
$T_J = -40^{\circ}\text{C to } +125^{\circ}\text{C}$		2.4	-	
$V_{\text{OUT}} = \text{Nominal Output Voltage}$	$V_{\text{IL}}$			
$T_J = 25^{\circ}\text{C}$		-	1.0	
$T_J = -40^{\circ}\text{C to } +125^{\circ}\text{C}$		-	0.8	
ON/OFF Pin Input Current				$\mu\text{A}$
ON/OFF Pin = 5.0 V ( "off" ), $T_J = 25^{\circ}\text{C}$	$I_{\text{IH}}$	-	30	
ON/OFF Pin = 0 V ( "on" ), $T_J = 25^{\circ}\text{C}$	$I_{\text{IL}}$	-	10	

3.The oscillator frequency reduces to approximately 18 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately 40% from the nominal output voltage. This self protection feature lowers the average dissipation of the IC by lowering the minimum duty cycle from 5% down to approximately 2%.

4.Output (Pin 2) sourcing current. No diode, inductor or capacitor connected to output pin.

5.Feedback (Pin 4) removed from output and connected to 0 V.

6.Feedback (Pin 4) removed from output and connected to +12 V for the Adjustable, 3.3 V, and 5.0 V versions, and +25 V for the 12 V and 15 V versions, to force the output transistor "off" .

7. $V_{\text{IN}} = 40 \text{ V}$ .

## Fixed Output Voltage Versions

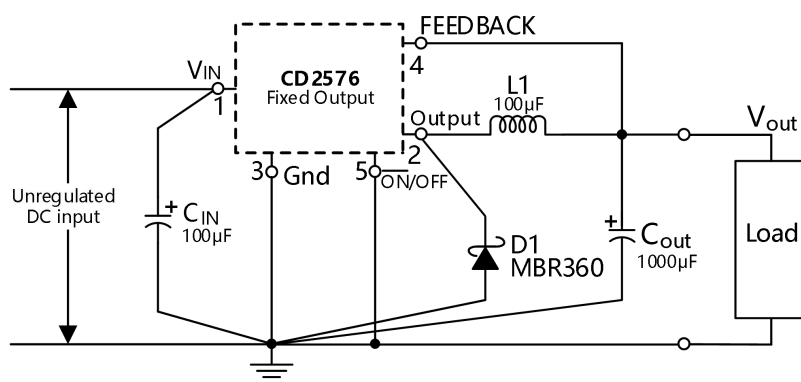


Figure 3.

$C_{\text{IN}}$  – 100 mF, 75 V, Aluminium Electrolytic

$C_{\text{OUT}}$  – 1000 mF, 25 V, Aluminium Electrolytic

$D_1$  – Schottky, MBR360

$L_1$  – 100 mH, Pulse Eng. PE-92108

$R_1$  – 2.0 k, 0.1%

$R_2 = 6.12 \text{ k}, 0.1\%$

## Adjustable Output Voltage Versions

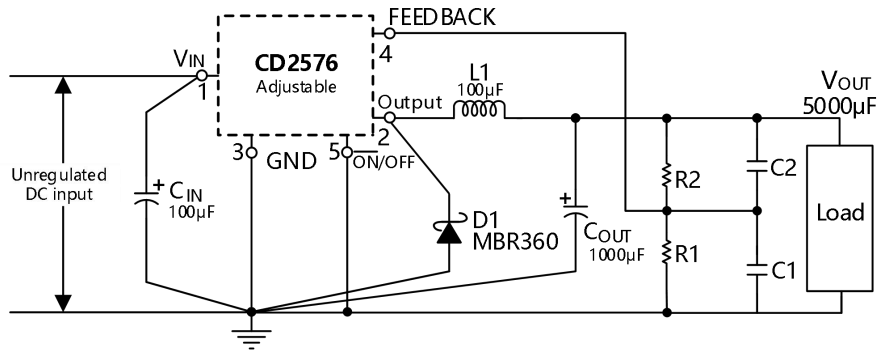


Figure 4.

$$V_{OUT} = V_{REF} \left( 1.0 + \frac{R_2}{R_1} \right), R_2 = R_1 \left( \frac{V_{OUT}}{V_{REF}} - 1.0 \right)$$

Where  $V_{ref} = 1.23 \text{ V}$ ,  $R_1$  between  $1.0 \text{ k}$  and  $5.0 \text{ k}$

$C_1 = 10 \text{ nF}$ ,  $C_2 = 1 \text{ nF}$

## Application Information

### INVERTING REGULATOR

Figure 5 shows a CD2576-12 in a buck-boost configuration to generate a negative 12V output from a positive input voltage. This circuit bootstraps the regulator's ground pin to the negative output voltage, then by grounding the feedback pin, the regulator senses the inverted output voltage and regulates it to  $-12\text{V}$ .

For an input voltage of  $12\text{V}$  or more, the maximum available output current in this configuration is approximately  $700 \text{ mA}$ . At lighter loads, the minimum input voltage required drops to approximately  $4.7\text{V}$ .

The switch currents in this buck-boost configuration are higher than in the standard buck-mode design, thus lowering the available output current. Also, the start-up input current of the buck-boost converter is higher than the standard buck-mode regulator, and this may overload an input power source with a current limit less than  $5\text{A}$ . Using a delayed turn-on or an undervoltage lockout circuit (described in the next section) would allow the input voltage to rise to a high enough level before the switcher would be allowed to turn on.

Because of the structural differences between the buck and the buck-boost regulator topologies, the buck regulator design procedure section can not be used to select the inductor or the output capacitor. The recommended range of inductor values for the buck-boost design is

between 68  $\mu$  H and 220  $\mu$  H, and the output capacitor values must be larger than what is normally required for buck designs. Low input voltages or high output currents require a large value output capacitor (in the thousands of micro Farads).

The peak inductor current, which is the same as the peak switch current, can be calculated from the following formula:

$$I_P \approx \frac{I_{LOAD}(V_{IN} + |V_O|)}{V_{IN}} + \frac{V_{IN}|V_O|}{V_{IN} + |V_O|} \times \frac{1}{2L_1f_{OSC}}$$

Where  $f_{osc} = 52$  kHz. Under normal continuous inductor current operating conditions, the minimum  $V_{IN}$  represents the worst case. Select an inductor that is rated for the peak current anticipated.

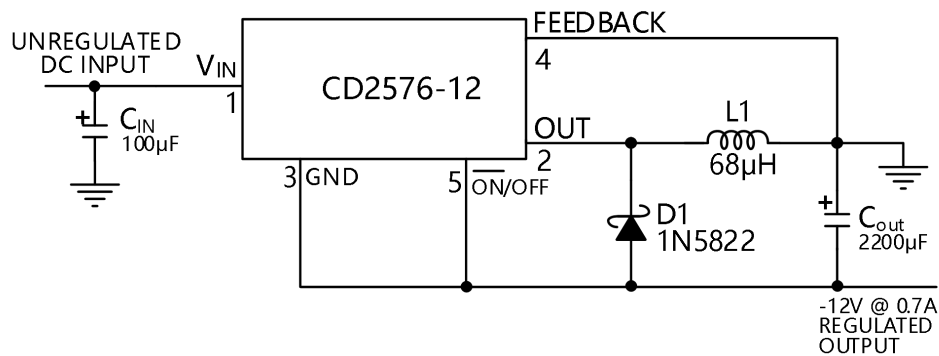
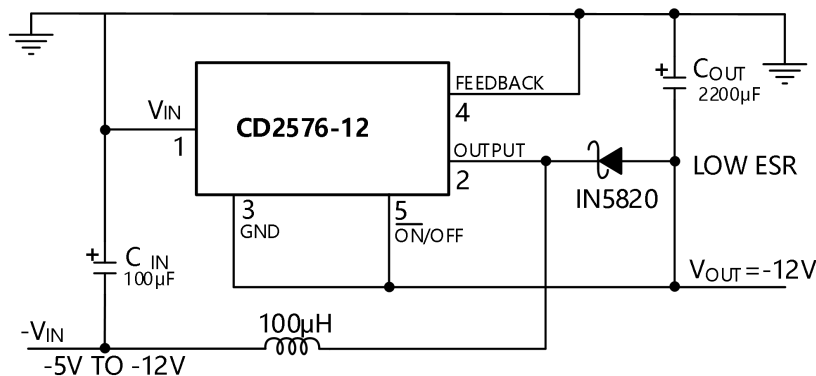


FIGURE 5. Inverting Buck-Boost Develops -12V

Also, the maximum voltage appearing across the regulator is the absolute sum of the input and output voltage. For a -12V output, the maximum input voltage for the CD2576 is +28V.

Another variation on the buck-boost topology is the negative boost configuration. The circuit in Figure 6 accepts an input voltage ranging from -5V to -12V and provides a regulated -12V output. Input voltages greater than -12V will cause the output to rise above -12V, but will not damage the regulator.



Typical Load Current

400 mA for  $V_{IN} = -5.2V$

750 mA for  $V_{IN} = -7V$

**Note:** Heat sink may be required.

FIGURE 6. Negative Boost

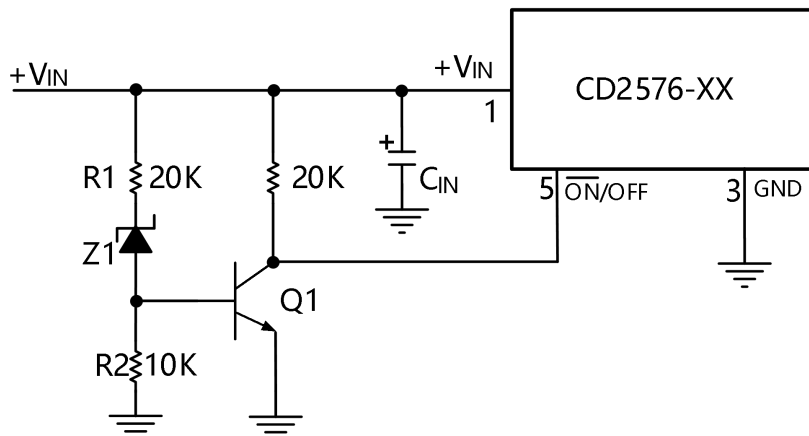
Because of the boosting function of this type of regulator, the switch current is relatively high, especially at low input voltages.

Output load current limitations are a result of the maximum current rating of the switch. Also, boost regulators can not provide current limiting load protection in the event of a shorted load, so some other means (such as a fuse) may be necessary.

## Undervoltage Lockout

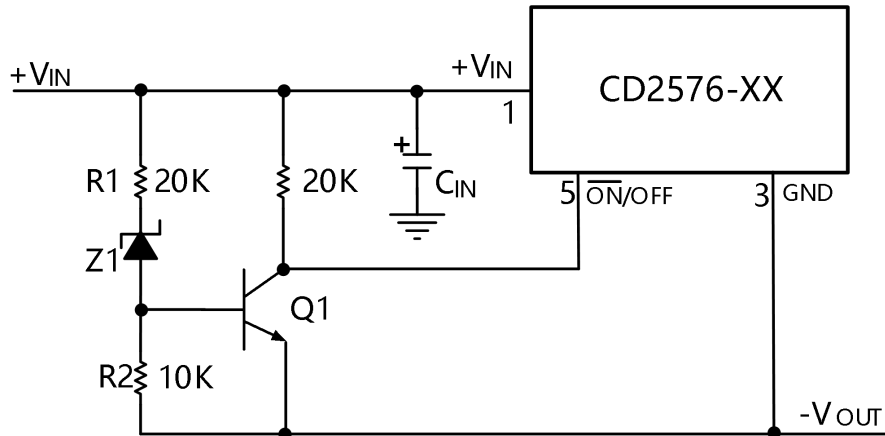
In some applications it is desirable to keep the regulator off until the input voltage reaches a certain threshold. An undervoltage lockout circuit which accomplishes this task is shown in Figure 7 while Figure 8 shows the same circuit applied to a buck-boost configuration. These circuits keep the regulator off until the input voltage reaches a predetermined level.

$$V_{TH} = V_{Z1} + 2V_{BE}(Q1)$$



Note: Complete circuit not shown.

FIGURE 7. Undervoltage Lockout for Buck Circuit



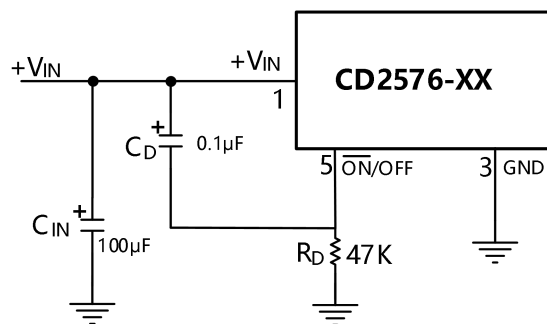
Note: Complete circuit not shown (see Figure 10).

FIGURE 8. Undervoltage Lockout for Buck-Boost Circuit

## Delayed Startup

The ON /OFF pin can be used to provide a delayed startup feature as shown in Figure 9. With an input voltage of 20V and for the part values shown, the circuit provides approximately 10 ms of delay time before the circuit begins switching.

Increasing the RC time constant can provide longer delay times. But excessively large RC time constants can cause problems with input voltages that are high in 60 Hz or 120 Hz ripple, by coupling the ripple into the ON /OFF pin.



Note: Complete circuit not shown.

FIGURE 9. Delayed Startup

## Adjustable Output, Low-Ripple Power Supply

A 3A power supply that features an adjustable output voltage is shown in Figure 10. An additional L-C filter that reduces the output ripple by a factor of 10 or more is included in this circuit.

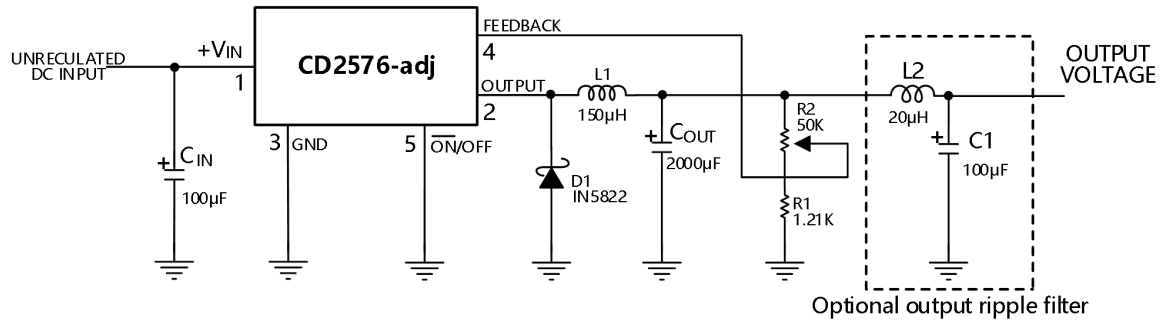


FIGURE 10. Adjustable 3A Power Supply with Low Output Ripple

## Definition of Terms

### BUCK REGULATOR

A switching regulator topology in which a higher voltage is converted to a lower voltage. Also known as a step-down switching regulator.

### BUCK-BOOST REGULATOR

A switching regulator topology in which a positive voltage is converted to a negative voltage without a transformer.

### DUTY CYCLE (D)

Ratio of the output switch' s on-time to the oscillator period.

$$\text{for buck regulator } D = \frac{t_{ON}}{T} = \frac{V_{OUT}}{V_{IN}}$$

$$\text{for buck-boost regulator } D = \frac{t_{ON}}{T} = \frac{|V_O|}{|V_O| + V_{IN}}$$

### CATCH DIODE OR CURRENT STEERING DIODE

The diode which provides a return path for the load current when the CD2576 switch is OFF.

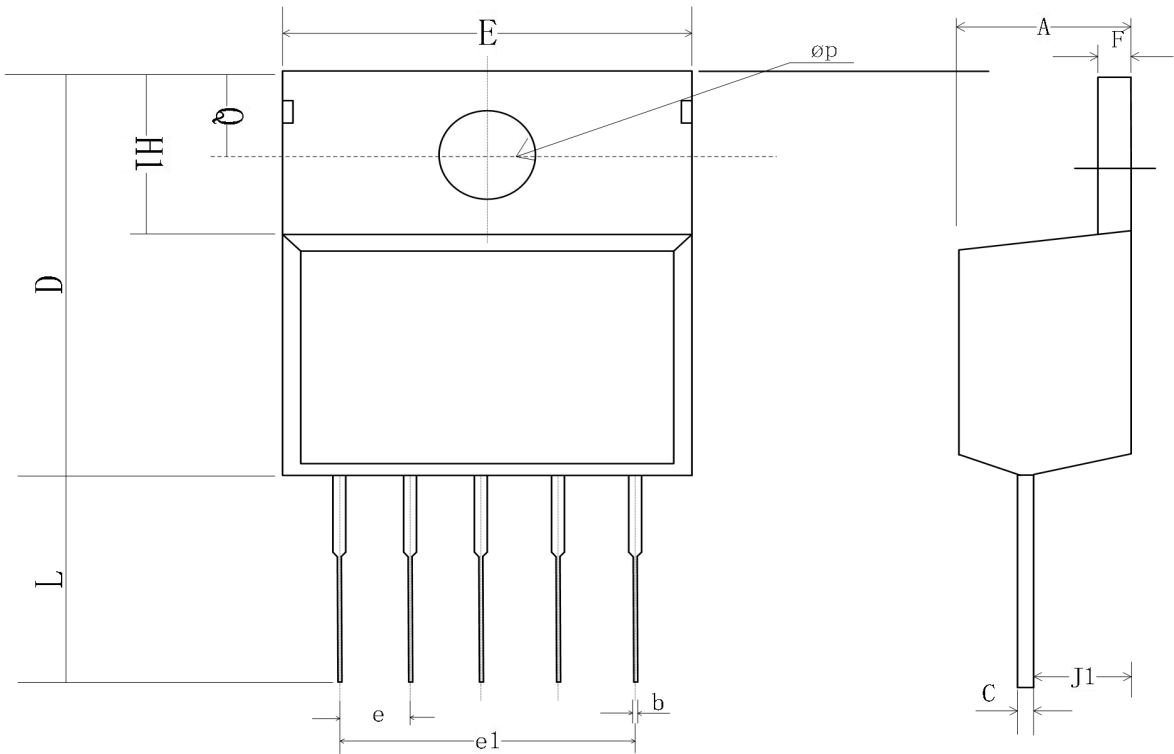
### EFFICIENCY ( $\eta$ )

The proportion of input power actually delivered to the load.

$$D = \frac{P_{OUT}}{P_{IN}} = \frac{P_{OUT}}{P_{OUT} + P_{LOSS}}$$

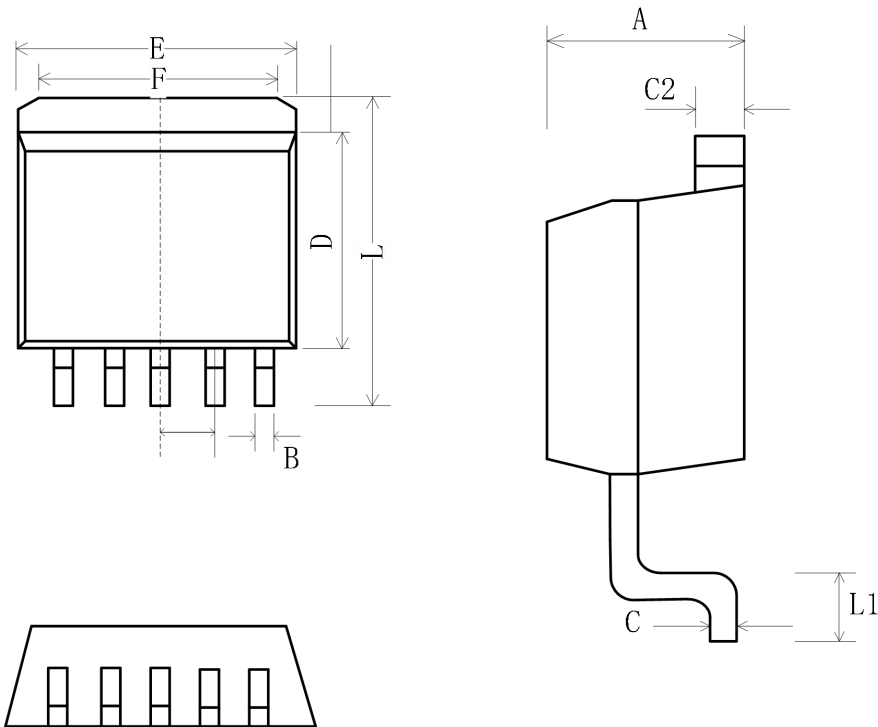
Package Outline Dimensions

TO-220-5L



SYMBOL	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.07	4.45	4.82	0.160	0.175	0.190
b	0.76	0.89	1.02	0.030	0.035	0.040
C	0.36	0.50	0.64	0.014	0.020	0.025
D	14.22	14.86	15.50	0.560	0.585	0.610
E	9.78	10.16	10.54	0.385	0.400	0.415
e	1.57	1.71	1.85	0.062	0.067	0.073
e1	6.68	6.81	6.93	0.263	0.268	0.273
F	1.14	1.27	1.40	0.045	0.050	0.055
H1	5.46	6.16	6.86	0.215	0.243	0.270
J1	2.29	2.74	3.18	0.090	0.108	0.125
L	13.21	13.97	14.73	0.520	0.550	0.580
$\phi p$	3.68	3.81	3.94	0.145	0.150	0.155
Q	2.54	2.73	2.92	0.100	0.107	0.115

TO-263-5L



SYMBOL	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.07	4.46	4.85	0.160	0.176	0.191
B	0.66	0.84	1.02	0.026	0.033	0.040
C	0.36	0.50	0.64	0.014	0.020	0.025
C2	1.14	1.27	1.40	0.045	0.050	0.055
D	8.65	9.15	9.65	0.341	0.360	0.380
E	9.78	10.16	10.54	0.385	0.400	0.415
e	1.57	1.71	1.85	0.062	0.068	0.073
F	6.60	6.86	7.11	0.260	0.270	0.280
L	14.61	15.24	15.88	0.575	0.600	0.625
L1	2.29	2.54	2.79	0.090	0.100	0.110
L2			2.92			0.115



Package/Ordering Information

MODEL	TEMPERATURE	PACKAGE DESCRIPTION	PACKAGE OPTION
CD2576DS-ADJ	-40°C~125°C	TO-220-5	Tube ,46
CD2576DT-ADJ	-40°C~125°C	TO-263-5	Tape and Reel, 500
CD72576DS-3.3	-40°C~125°C	TO-220-5	Tube ,46
CD2576DT-3.3	-40°C~125°C	TO-263-5	Tape and Reel, 500
CD2576DS-5.0	-40°C~125°C	TO-220-5	Tube ,46
CD2576DT-5.0	-40°C~125°C	TO-263-5	Tape and Reel, 500
CD2576DS-12	-40°C~125°C	TO-220-5	Tube ,46
CD2576DT-12	-40°C~125°C	TO-263-5	Tape and Reel, 500
CD2576DS-15	-40°C~125°C	TO-220-5	Tube ,46
CD2576DT-15	-40°C~125°C	TO-263-5	Tape and Reel, 500

Revision Log

Version	Revision date	Change content	Reason for Change	Modified by	Reviewed By	Note
V1.0	2025.6.12	Initial version	Regular update	WW	LYL	